

1
SCALE 1:1

SOIC-16 WB
CASE 751G
ISSUE E

DATE 08 OCT 2021

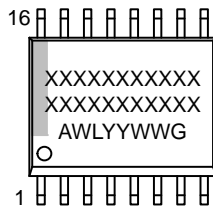
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**GENERIC
MARKING DIAGRAM***



- XXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb iFree Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb iFree indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.