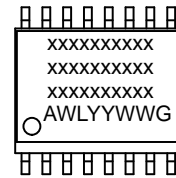


SCALE 1:1

SOIC 16 WB, EP
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DATE 23 OCT 2015



- xxx = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb iFree Package

*This information is generic. Please refer to