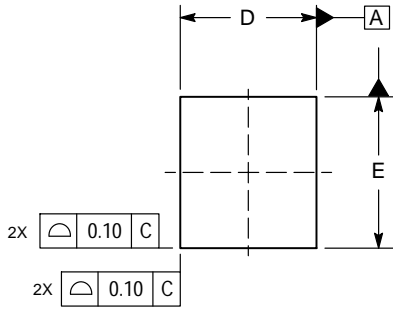


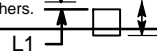
UDFN6, 1.8x2, 0.5P
CASE 517BA i01
ISSUE A



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND

DOCUMENT NUMBER:	98AON44546E	Electronic versions are uncontrolled except when accessed directly from the ON Semiconductor website.
DESCRIPTION:		

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States of America. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee of the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product. ON Semiconductor disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license, express or implied, by its sale of products. © 2008 ON Semiconductor. All rights reserved.

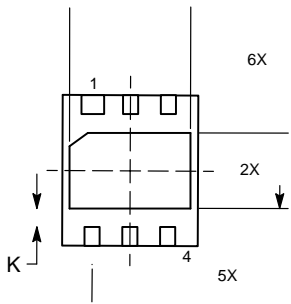


DETAIL A
OPTIONAL

0.20	0.40
1.80 BSC	
1.50	1.70
2.00 BSC	
0.90	1.10
0.50 BSC	
0.15	0.35

GENERIC
MARKING DIAGRAM*

XXXX = Specific Device Code
MM = Lot Number



0.05	C
------	---

*For additional information on our Pb-free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.