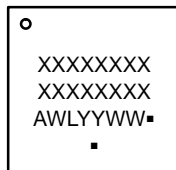


WQFNW39 5x6, 0.45P
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PIN ONE
LD

GENERIC
MARKING DIAGRAM*



XXXX = Specific Device Code

A = Assembly Location

WL = Wafer Lot

YY = Year

WW = Work Week

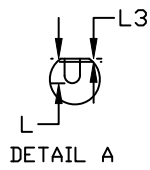
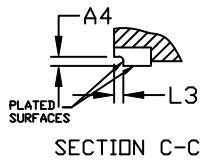
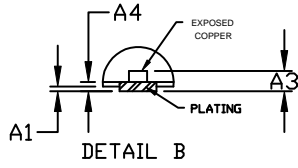
▪ = Pb iFree Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb iFree indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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RECOMMENDED
MOUNTING FOOTPRINT

* For ϕ _____