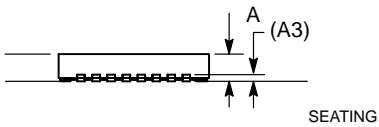
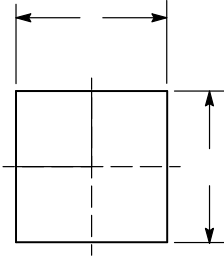


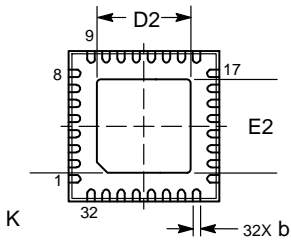
QFNW32 7x7, 0.65P  
CASE 484AG  
ISSUE A

DATE 07 AUG 2018

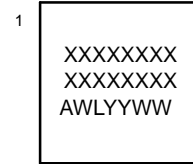
SCALE 2:1



NOTE 4



GENERIC  
MARKING DIAGRAM\*



- XXXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- = Pb Free Package