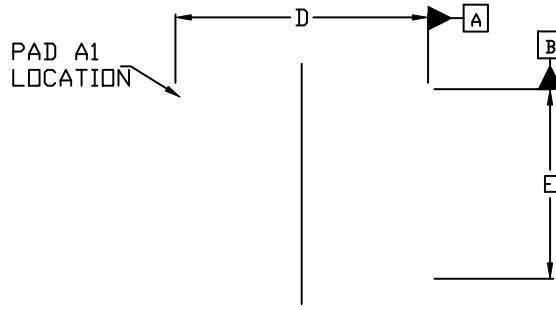
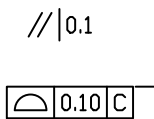


**SIP51 8x6**  
**CASE 127EY**  
**ISSUE B**

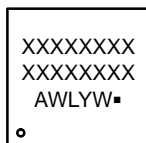
DATE 30 JUN 2020



TOP VIEW



**GENERIC MARKING DIAGRAM\***



- A = Assembly Location
- WL = Wafer Lot
- Y = Year
- W = Work Week
- = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.