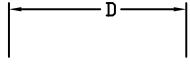
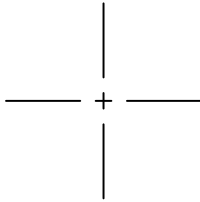


LCC48, 14.22x14.22  
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DATE 31 JAN 2012



4. THE LID DEFINED BY DIMENSIONS D2 AND E<sup>2</sup>



**GENERIC  
MARKING DIAGRAM**

XXXXX = Specific Device Code  
A = Assembly Location  
WL = Wafer Lot  
YY = Year  
WW = Work Week  
NNNN = Serial Number