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25 PAD
HYBRID
CASE 127DN

PAD CONNECTION

Features

MARKING DIAGRAM



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RHYTHM SB3231

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Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Conditions	Min	Typ	Max	Units
OUTPUT						
	-	-	-		-	
		-	-			Ω
CONTROL A/D						
	-	-		-	-	
	-					

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Table 3. I²C TIMING

Parameter	Symbol	Standard Mode		Fast Mode		Units
		Min	Max	Min	Max	
			-		-	μ

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TYPICAL APPLICATIONS (continued)

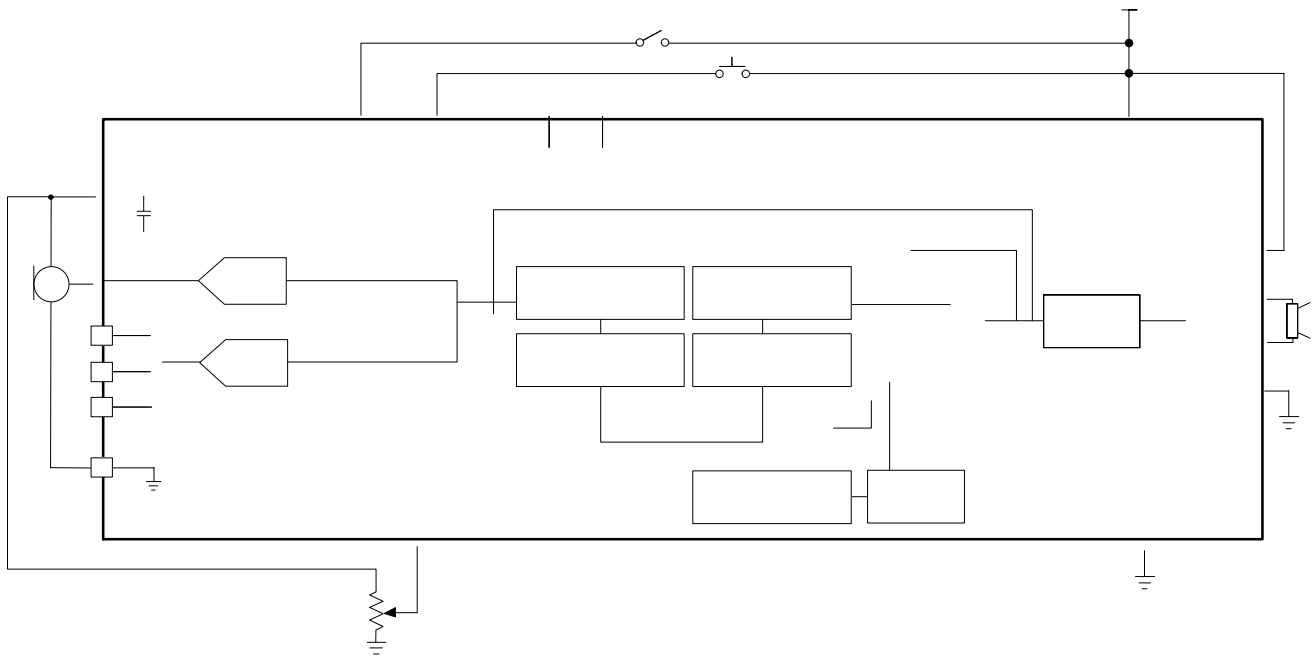


Figure 3. Typical Programmable Application Circuit

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RHYTHM SB3231 OVERVIEW

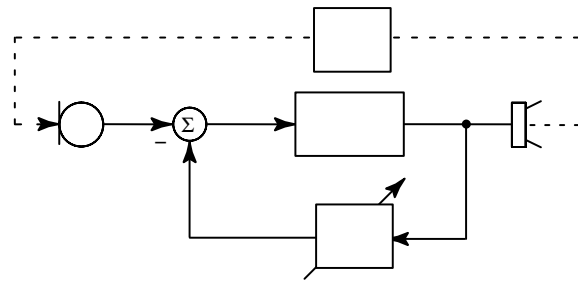


Figure 6. Adaptive Feedback Canceller (AFC)
Block Diagram

Feedback Path Measurement Tool

Automatic Telecoil

Adaptive Noise Reduction

Adaptive Feedback Canceller

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Memory Select Switches

Table 4. MS SWITCH MODES

MS Switch Mode	MS1 Switch	MS2 Switch	Max # of Valid Memories	Only	MSSMode	Use

Table 5. MS SWITCH LOGIC LEVELS VS. IDS PULLUPDOWN SETTINGS

“PullUpDown” Setting in IDS	MS Switch State	MS Input Logic Level	Switch Connection



Deep Reset Mode

Advanced Reset Mode

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ORDERING INFORMATION

Device	Package	Shipping
-		
- -		

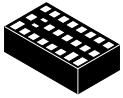
Hybrid Jig Ordering Information

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PAD LOCATIONS

Table 9. PAD POSITION AND DIMENSIONS

Pad No.	Pad Position	Pad DimensionsP	a	d
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SCALE 2:1

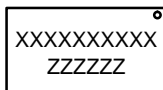
SIP25, 5.59x3.18
CASE 127DN
ISSUE A

DATE 21 JUL 2020

CROWNS OF THE PADS.

RECOMMENDED
MOUNTING FOOTPRINT

**GENERIC
MARKING DIAGRAM***



XX = Specific Device Code
ZZ = Lot Traceability

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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