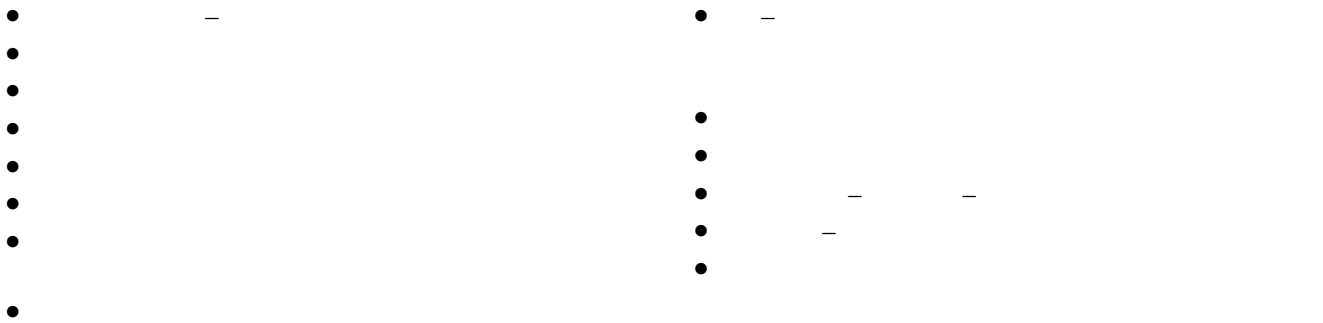


DEVICE NOT RECOMMEN

NEW DES

RHYTHM SB3230



BLOCK DIAGRAM

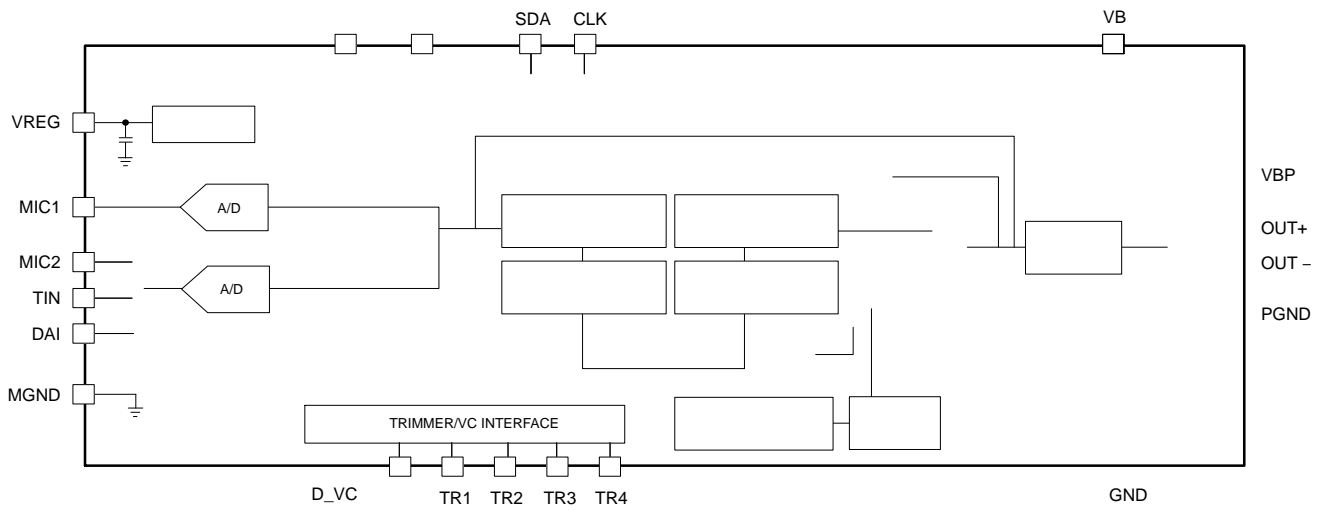


Figure 1. Hybrid Block Diagram

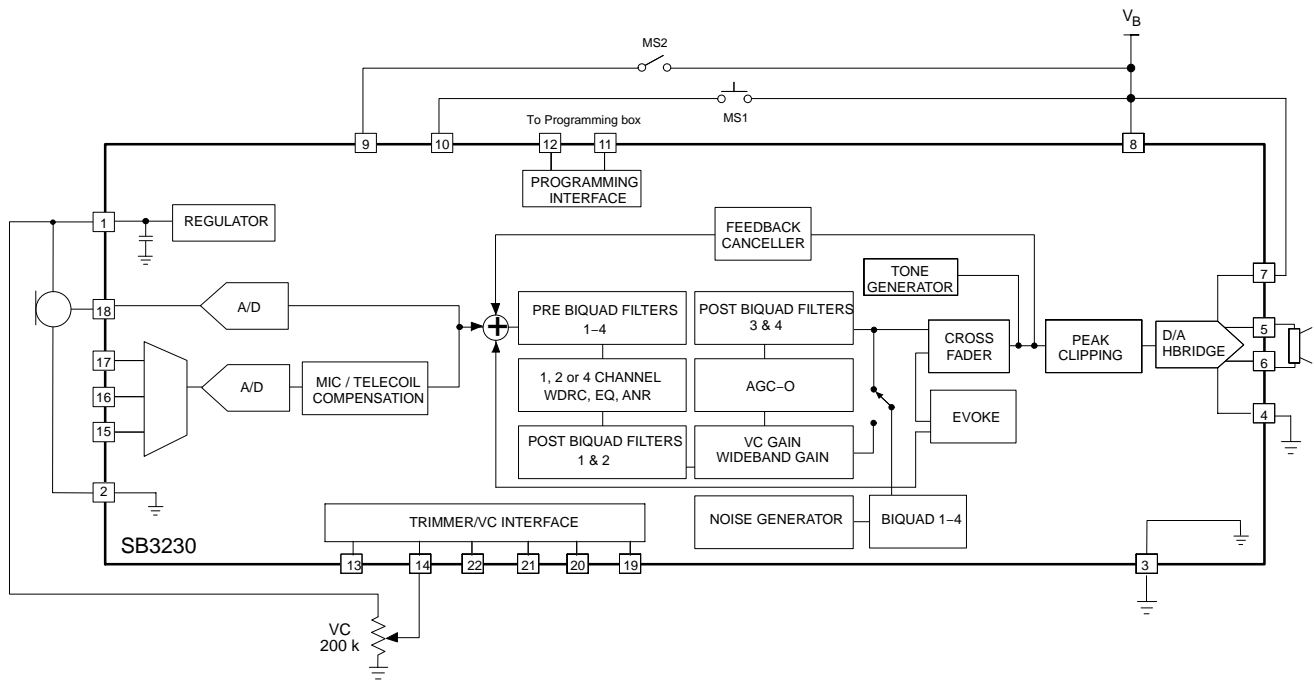
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SPECIFICATIONS

Table 3. I²C TIMING

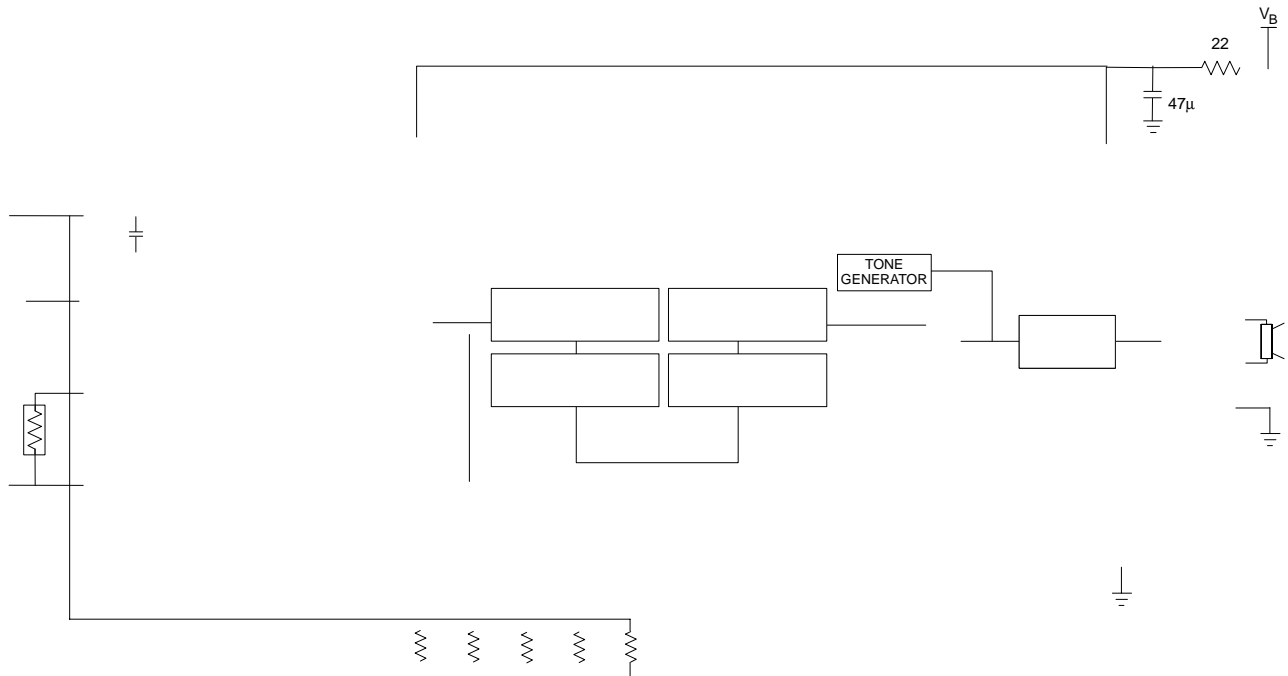
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TYPICAL APPLICATIONS (continued)



Note: All resistors in ohms and all capacitors in farads, unless otherwise stated.

Figure 3. Typical Programmable Application Circuit



Note: All resistors in ohms and all capacitors in farads, unless otherwise stated.

Figure 4. Typical Trimmer Application Circuit

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SB3230 OVERVIEW

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Automatic Telecoil

Adaptive Feedback Cancellor

Volume Control, Trimmers and Switches

External Volume Control

Analog Volume Control



Trimmers



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Case 1. Pull-up/Pull-down Resistors set to Pull-down

RHYTHM SB3230

lower

-
-
-

AGC-O

-

Power Management

-

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PAD LOCATIONS

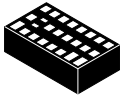
Table 8. PAD POSITION AND DIMENSIONS

Pad No.	Pad Position	Pad DimensionsP	a	d
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Table 8. PAD POSITION AND DIMENSIONS

Pad No.	X	Y	Xdim (mm)	Ydim (mm)
1	0	0	0.508	0.838
2	-0.686	0	0.508	0.838
3	-1.372	-0.127	0.508	0.584
4	-2.057	-0.127	0.508	0.584
5	-2.743	-0.127	0.508	0.584
6	-3.429	-0.127	0.508	0.584
7	-4.115	-0.127	0.508	0.584
8	-4.801	0	0.508	0.838
9	-4.801	1.067	0.508	0.584
10	-4.801	2.159	0.508	0.584
11	-4.115	2.159	0.508	0.584
12	-3.429	2.159	0.508	0.584
13	-2.743	2.159	0.508	0.584
14	-2.057	2.159	0.508	0.584
15	-1.372	2.159	0.508	0.584
16	-0.686	2.159	0.508	0.584
17	0	2.159	0.508	0.584
18	0	1.067	0.508	0.584
19	-0.686	1.067	0.508	0.584
20	-1.372	1.067	0.508	0.584
21	-2.057	1.067	0.508	0.584
22	-2.743	1.067	0.508	0.584
23	-3.429	1.067	0.508	0.584
24	-4.115	0.673	0.457	0.305
25	-4.115	1.359	0.457	0.305



SCALE 2:1

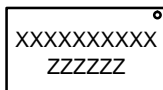
SIP25, 5.59x3.18
CASE 127DN
ISSUE A

DATE 21 JUL 2020

CROWNS OF THE PADS.

RECOMMENDED
MOUNTING FOOTPRINT

**GENERIC
MARKING DIAGRAM***



XX = Specific Device Code
ZZ = Lot Traceability

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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