

MOSFET – Power, Single

N-Channel

100 V, 20 m Ω , 41 A

NVD6824NL

Features

- ✗ Low $R_{DS(on)}$ to Minimize Conduction Losses
- ✗ High Current Capability
- ✗ Avalanche Energy Specified
- ✗ AEC iQ101 Qualified
- ✗ These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS ($T_J = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
t			

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 V, I_D = 250 A$	100			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$V_{(BR)DSS}/T_J$			92		$mV/^\circ C$
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0 V,$	$T_J = 25^\circ C$		1.0	A
		$V_{DS} = 100 V$	$T_J = 125^\circ C$		100	
Gate-to-Source Leakage Current	I_{GSS}	$V_{DS} = 0 V, V_{GS} = -20 V$				

TYPICAL CHARACTERISTICS

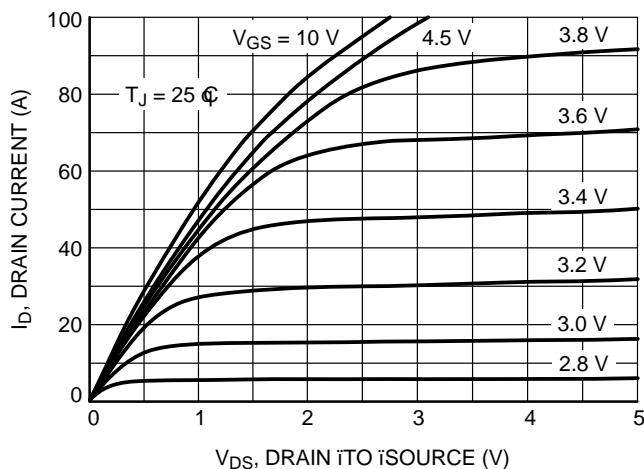


Figure 1. On Region Characteristics

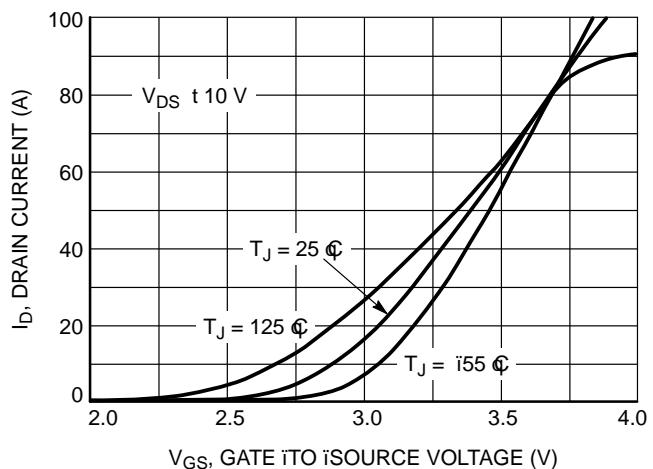


Figure 2. Transfer Characteristics

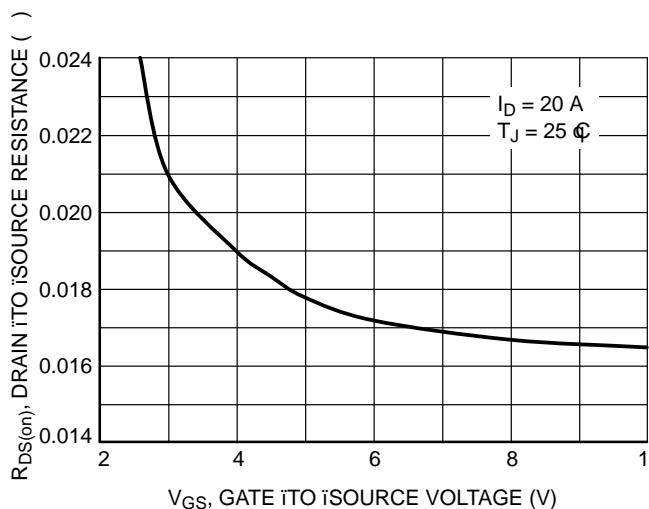


Figure 3. On Resistance vs. Gate Voltage

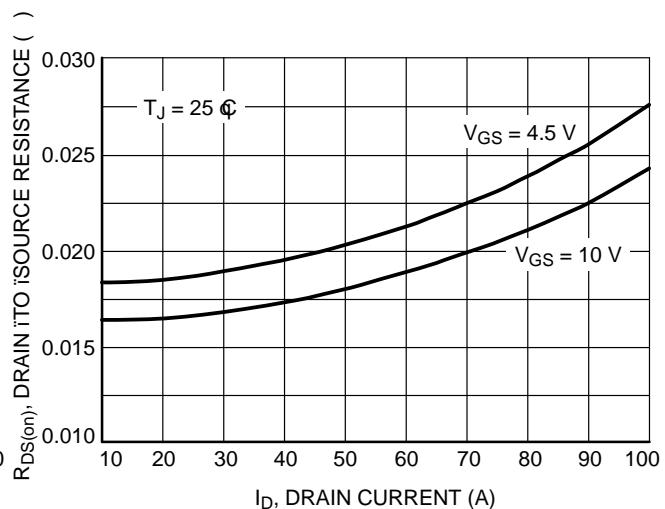


Figure 4. On Resistance vs. Drain Current and Gate Voltage

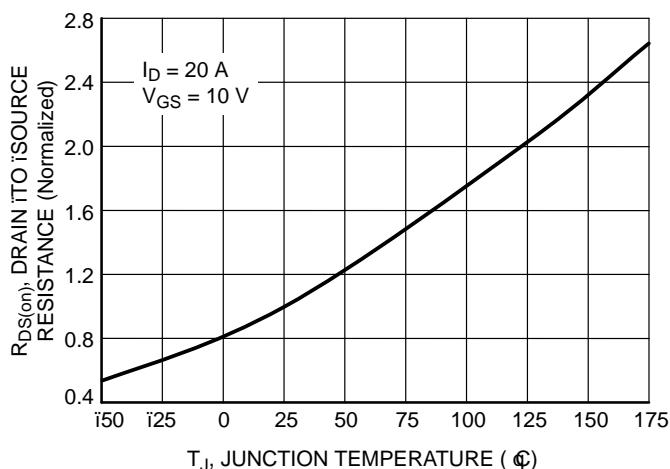


Figure 5. On Resistance Variation with Temperature

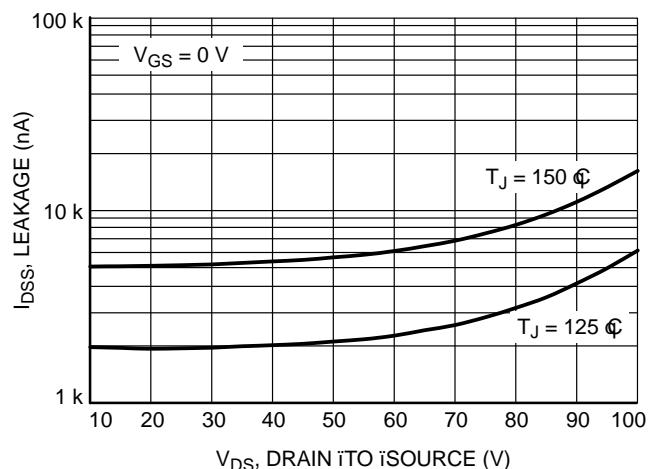


Figure 6. Drain-to-Source Leakage Current vs. Voltage

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TYPICAL CHARACTERISTICS

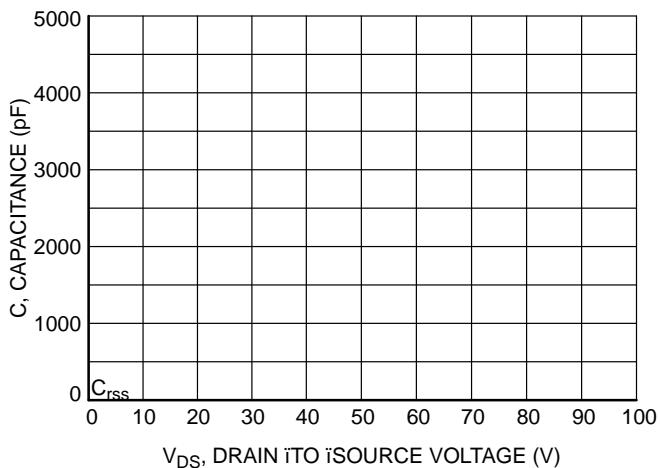


Figure 7. Capacitance Variation

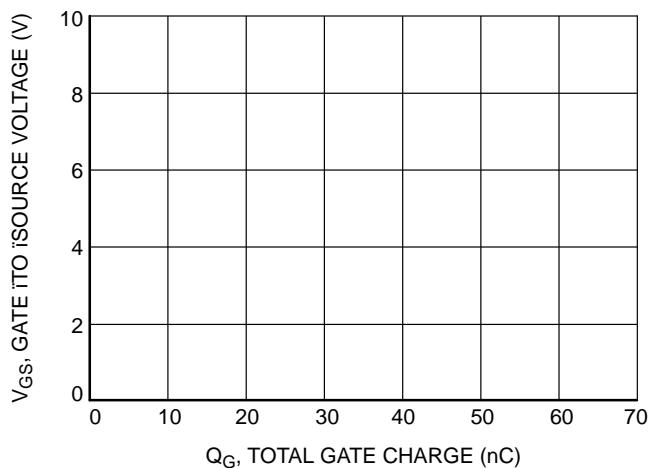


Figure 8. Gate-to-Source Voltage vs. Total Charge

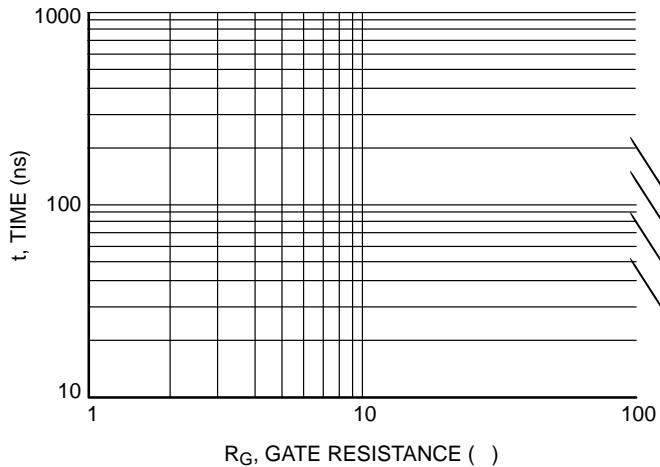


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

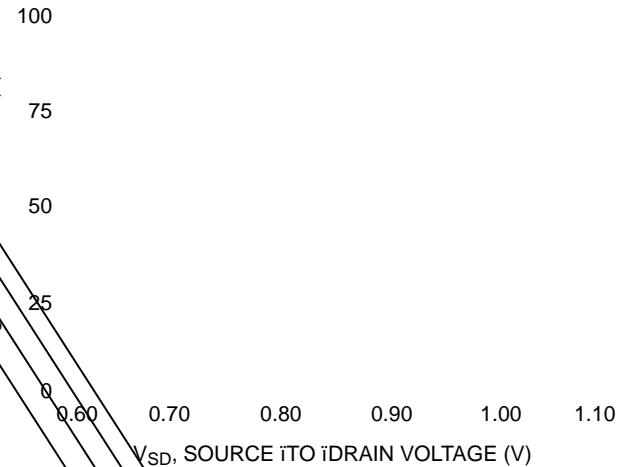


Figure 10. Diode Forward Voltage vs. Current

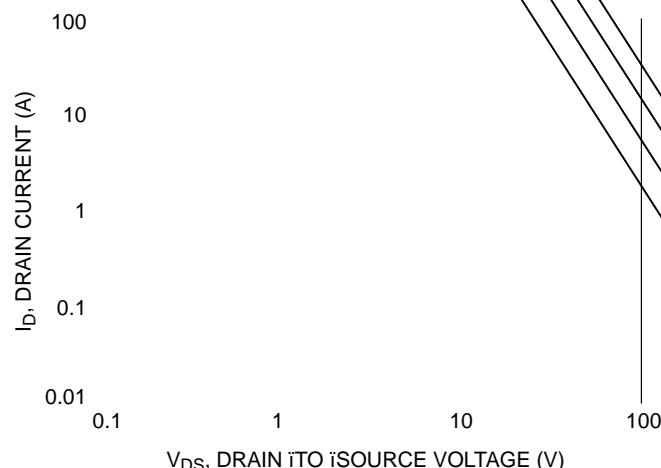


Figure 11. Maximum Rated Forward Biased Safe Operating Area

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TYPICAL CHARACTERISTICS

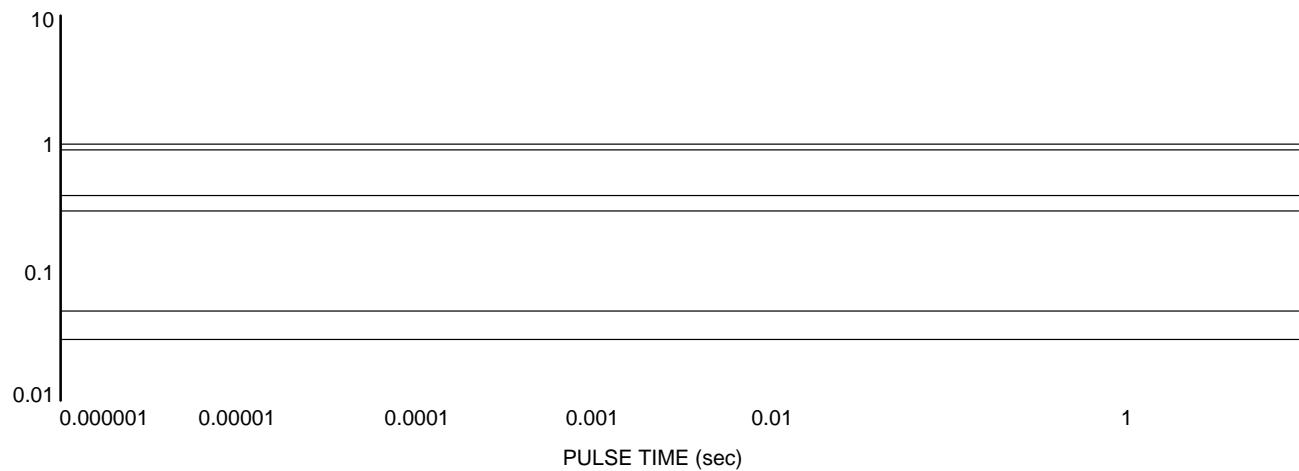


Figure 12. Thermal Response

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PACKAGE DIMENSIONS



DPAK (SINGLE GAUGE)
CASE 369C
ISSUE G

DATE 31 MAY 2023

SCALE 1:1

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