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Silicon Carbide (SiC)
MOSFET - EliteSiC,
 23 mohm, 650 V, M3S,
 D2PAK-7L

NVBG023N065M3S

Features

- Typical $R_{DS(ON)} = 23 \text{ m}\Omega$ @ $V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge ($Q_{G(tot)} = 69 \text{ nC}$)
- High Speed Switching with Low Capacitance ($C_{oss} = 153 \text{ pF}$)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- This Device is Halide Free and RoHS Compliant with Exemption 7a, Pb-Free 2LI (on Second Level Interconnection)

Applications

- Automotive On Board Charger
- Automotive DC-DC Converter for EV/HEV

MAXIMUM RATINGS ($T_J = 25 \text{ C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	650	V
Gate-to-Source Voltage	V_{GS}	-8/+22	
Continuous Drain Current	I_D	70	A

$T_C = 25 \text{ C}$

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ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ C}$ unless otherwise specified) (continued)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
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SWITCHING CHARACTERISTICS

Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = -3/18\text{ V}$, $I_D = 20\text{ A}$, $V_{DD} = 400\text{ V}$, $R_G = 4.7\ \Omega$, $T_J = 175\text{ C}$ (Note 4, 5)	-	9.6	-	ns
Turn-Off Delay Time	$t_{d(OFF)}$		-	41	-	
Rise Time	t_r		-	14	-	
Fall Time	t_f		-	12	-	
Turn-On Switching Loss	E_{ON}		-	51	-	μJ
Turn-Off Switching Loss	E_{OFF}		-	45	-	
Total Switching Loss	E_{TOT}		-	96	-	

SOURCE-TO-DRAIN DIODE CHARACTERISTICS

Forward Diode Voltage	V_{SD}	$I_{SD} = 20\text{ A}$, $V_{GS} = -3\text{ V}$, $T_J = 25\text{ C}$	-	4.5	6.0	V
		$I_{SD} = 20\text{ A}$, $V_{GS} = -3\text{ V}$, $T_J = 175\text{ C}$ (Note 5)	-	4.2	-	
Reverse Recovery Time	t_{RR}	$V_{GS} = -3\text{ V}$, $I_S = 20\text{ A}$, $dI/dt = 1000\text{ A}/\mu\text{s}$, $V_{DS} = 400\text{ V}$, $T_J = 25\text{ C}$ (Note 5)	-	19	-	ns
Charge time	t_a		-	11	-	
Discharge time	t_b		-	8	-	
	Q_{RR}		-	97	-	nC

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TYPICAL CHARACTERISTICS

Figure 1. Output Characteristics

Figure 2. Output Characteristics

0

Figure 3. Transfer Characteristics

Figure 4. On-Resistance vs. Gate Voltage

Figure 5. On-

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TYPICAL CHARACTERISTICS

0.1

V_{DS} , Drain to Source Voltage (V)

Figure 7. Capacitance Characteristics

V_{DS} , Drain to Source Voltage (V)

Figure 8. Stored Energy vs. Drain to Source Voltage

Figure 9. Gate Charge Characteristics

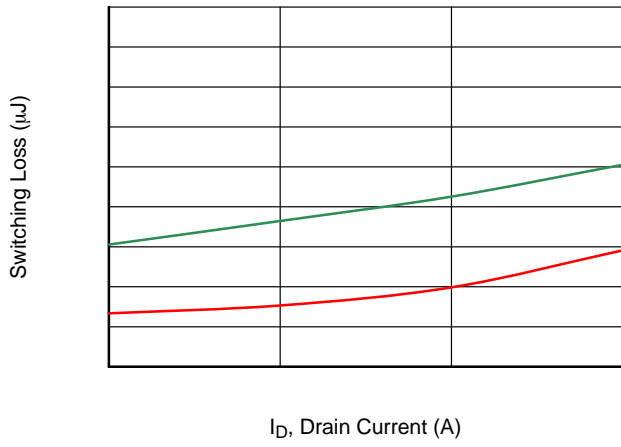
Figure 10. Reverse Conduction Characteristics

Figure 11. Reverse Conduction Characteristics

Figure 12. Safe Operating Area

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TYPICAL CHARACTERISTICS



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D²PAK7 (TO-263-7L HV)
CASE 418BJ
ISSUE B

DATE 16 AUG 2019

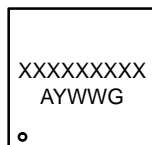
A

c2

H

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**GENERIC
MARKING DIAGRAM***



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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