## **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

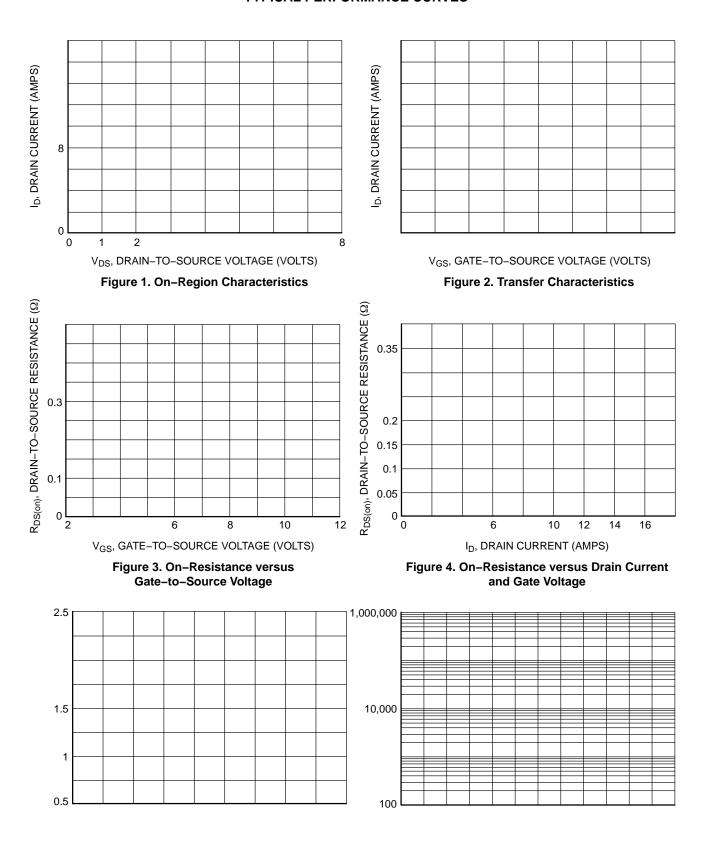
Characteristic	Symbol	Min	Тур	Max	Unit	Ī
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**OFF CHARACTERISTICS** 

### **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Characteristic			Min	Тур	Max	Unit		
SWITCHING CHARACTERISTICS (Note 4)								
Gate Charge		Q <sub>T</sub>	-	3.6	-	nC		
	(V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1.5 A) (Note 3)	Q <sub>1</sub>	-	1.0	-			

#### **TYPICAL PERFORMANCE CURVES**



#### **TYPICAL PERFORMANCE CURVES**

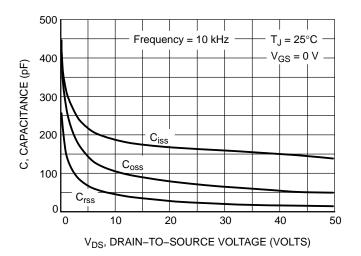


Figure 7. Capacitance Variation

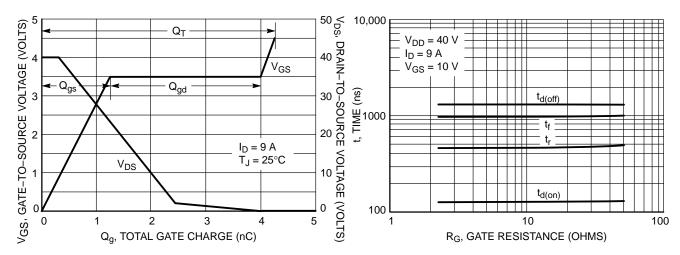


Figure 8. Gate-To-Source and Drain-To-Source Voltage versus Total Charge

Figure 9. Resistive Switching Time Variation versus Gate Resistance

### DRAIN-TO-SOURCE DIODE CHARACTERISTICS

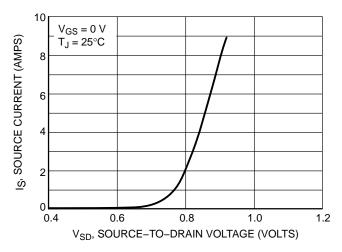


Figure 10. Diode Forward Voltage versus Current

# 4 1 2 3 3 SCALE 1:1

### DPAK (SINGLE GAUGE) CASE 369C ISSUE G

DATE 31 MAY 2023

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:
PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. GATE
2. COLLECTOR	2. DRAIN	2. CATHODE	2. ANODE	2. ANODE
<ul><li>3. EMITTER</li><li>4. COLLECTOR</li></ul>	<ol> <li>SOURCE</li> <li>DRAIN</li> </ol>	<ol> <li>ANODE</li> <li>CATHODE</li> </ol>	3. GATE 4. ANODE	<ol> <li>CATHODE</li> <li>ANODE</li> </ol>

STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:
PIN 1. MT1	PIN 1. GATE	PIN 1. N/C	PIN 1. ANODE	PIN 1. CATHODE
2. MT2 3. GATE 4. MT2	<ol> <li>COLLECTOR</li> <li>EMITTER</li> <li>COLLECTOR</li> </ol>	<ol> <li>CATHODE</li> <li>ANODE</li> <li>CATHODE</li> </ol>	<ul><li>2. CATHODE</li><li>3. RESISTOR ADJUST</li><li>4. CATHODE</li></ul>	<ul><li>2. ANODE</li><li>3. CATHODE</li><li>4. ANODE</li></ul>

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