

# NE592

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## V A

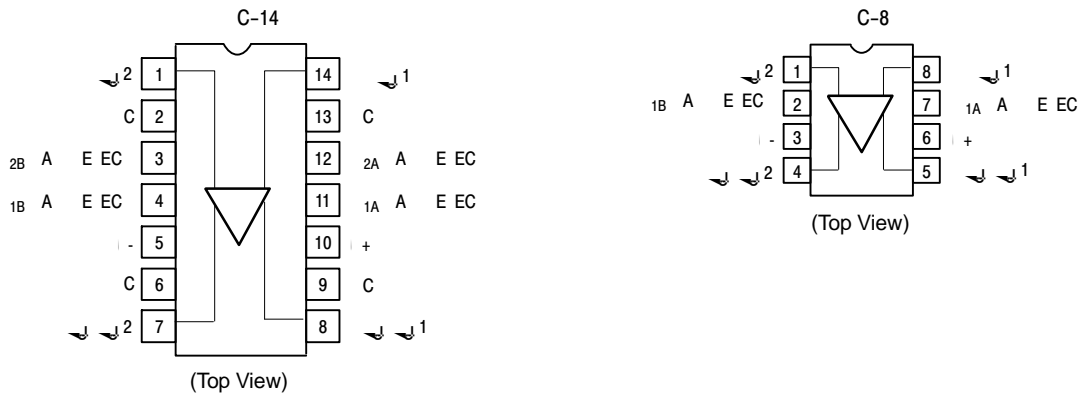
The NE592 is a monolithic, two-stage, differential output, wideband video amplifier. It offers fixed gains of 100 and 400 without external components and adjustable gains from 400 to 0 with one external resistor. The input stage has been designed so that with the addition of a few external reactive elements between the gain select terminals, the circuit can function as a high-pass, low-pass, or band-pass filter. This feature makes the circuit ideal for use as a video or pulse amplifier in communications, magnetic memories, display, video recorder systems, and floppy disk head amplifiers. Now available in an 8-pin version with fixed gain of 400 without external components and adjustable gain from 400 to 0 with one external resistor.

### Features

- 120 MHz Unity Gain Bandwidth
- Adjustable Gains from 0 to 400
- Adjustable Pass Band
- No Frequency Compensation Required
- Wave Shaping with Minimal External Components
-

# NE592

## PIN CONNECTIONS



### MAXIMUM RATINGS ( $T_A = +25^\circ\text{C}$ , unless otherwise noted.)

Rating	Symbol	Value	Unit
Supply Voltage	$V_{CC}$	$\pm 8.0$	V
Differential Input Voltage	$V_{IN}$	$\pm 5.0$	V
Common-Mode Input Voltage	$V_{CM}$	$\pm 6.0$	V
Output Current	$I_{OUT}$	10	mA
Operating Ambient Temperature Range	$T_A$	0 to +70	$^\circ\text{C}$
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	65 to +150	$^\circ\text{C}$
Maximum Power Dissipation, $T_A = 25^\circ\text{C}$ (Still Air) (Note 1)	$P_{D\ MAX}$	SOIC-14 Package 0.98 SOIC-8 Package 0.79	W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	SOIC-14 Package 145 SOIC-8 Package 182	$^\circ\text{C/W}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Derate above  $25^\circ\text{C}$  at the following rates:  
SOIC-14 package at  $6.9\ \text{mW}/^\circ\text{C}$   
SOIC-8 package at  $5.5\ \text{mW}/^\circ\text{C}$

## NE592

**DC ELECTRICAL CHARACTERISTICS** ( $V_{SS} = \pm 6.0\text{ V}$ ,  $V_{CM} = 0$ , typicals at  $T_A = +25^\circ\text{C}$ , min and max at  $0^\circ\text{C}$ ,  $T_A = 70^\circ\text{C}$ , unless otherwise noted. Recommended operating supply voltages  $V_S = \pm 6.0\text{ V}$ .)

Characteristic	Test Conditions	Symbol	Min	Typ	Max	Unit
Differential Voltage Gain Gain 1 (Note 2) Gain 2 (Notes 3 and 4)	$R_L = 2.0\text{ k}\Omega$ , $V_{OUT} = 3.0\text{ V}_{P-P}$	$A_{VOL}$	250 80	400 100	600 120	V/V
Input Resistance Gain 1 (Note 2) Gain 2 (Notes 3 and 4)	$T_A = 25^\circ\text{C}$ $0^\circ\text{C}$ $T_A$ $70^\circ\text{C}$	$R_{IN}$	– 10 8.0	4.0 30 –	– – –	k $\Omega$
Input Capacitance	Gain 2 (Note 4)	$C_{IN}$	–	2.0	–	pF
Input Offset Current	$T_A = 25^\circ\text{C}$ $0^\circ\text{C}$ $T_A$ $70^\circ\text{C}$	$I_{OS}$	– –	0.4 –	5.0 6.0	$\mu\text{A}$
Input Bias Current	$T_A = 25^\circ\text{C}$ $0^\circ\text{C}$ $T_A$ $70^\circ\text{C}$	$I_{BIAS}$	– –	9.0 –	30 40	$\mu\text{A}$
Input Noise Voltage	BW 1.0 kHz to 10 MHz	$V_{NOISE}$	–	12	–	$\mu\text{V}$

# NE592

## TYPICAL PERFORMANCE CHARACTERISTICS

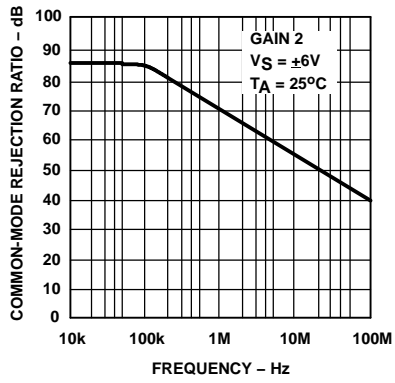


Figure 2. Common-Mode Rejection Ratio as a Function of Frequency

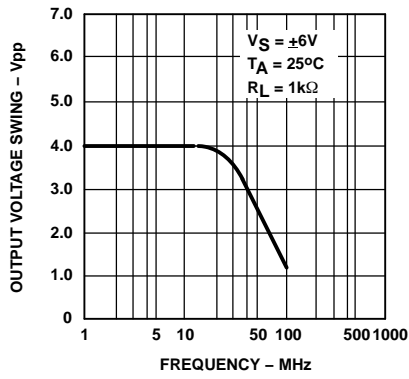


Figure 3. Output Voltage Swing as a Function of Frequency

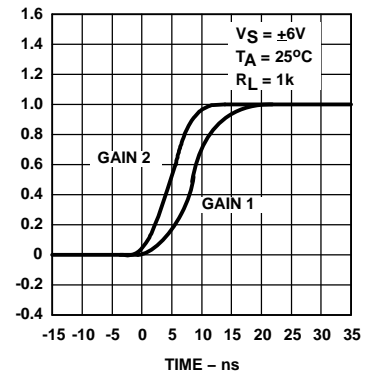
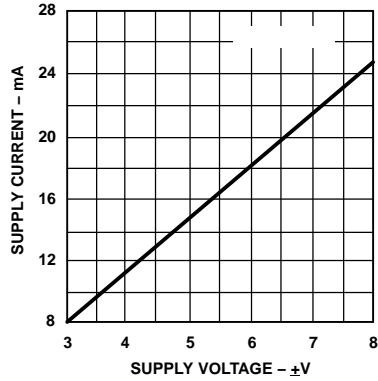


Figure 4. Pulse Response





TYPICAL PERFORMANCE CHARACTERISTICS

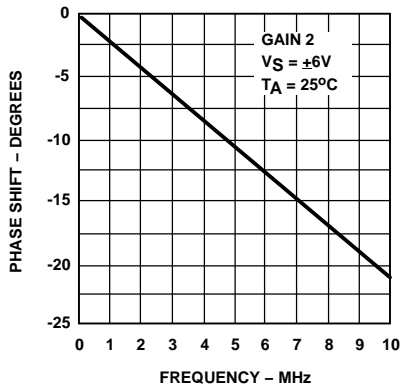


Figure 20. Phase Shift as a Function of Frequency

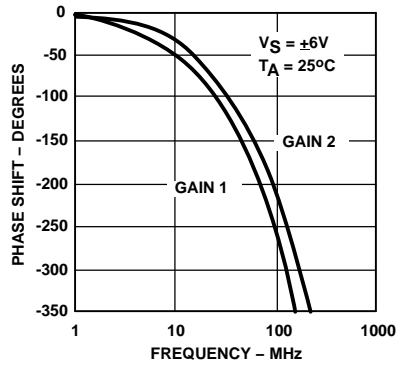


Figure 21. Phase Shift as a Function of Frequency

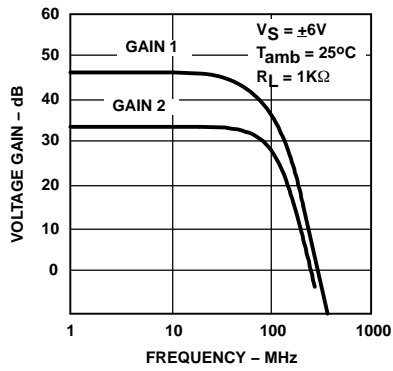


Figure 22. Voltage Gain as a Function of Frequency

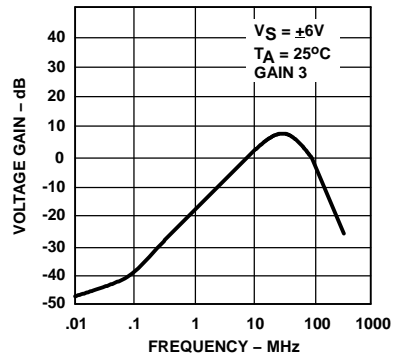
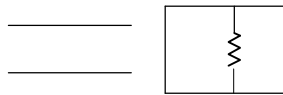
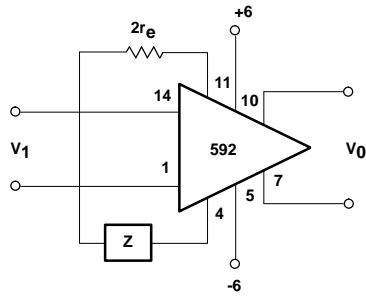


Figure 23. Voltage Gain as a Function of Frequency

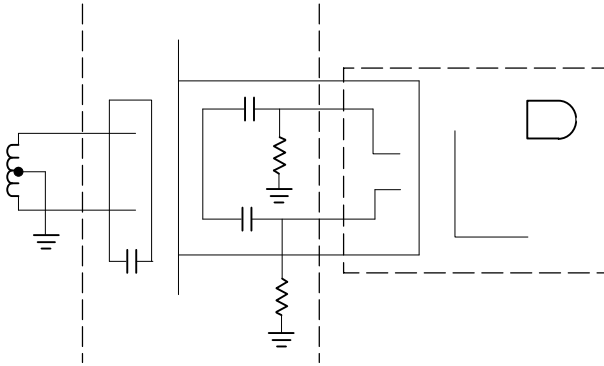


# NE592

NOTE:



Basic Configuration



Disc/Tape Phase-Modulated Readback Systems

NOTE:

For frequency  $F_1 \ll 1/2 \pi (32) C$

$$V_O = 1.4 \times 10^4 C \frac{dV_i}{dT}$$

Differentiation with High  
Common-Mode Noise Rejection

# NE592

## ORDERING INFORMATION

Device	Temperature Range	Package	Shipping†
NE592D8G	0 to +70°C	SOIC-8 (Pb-Free)	98 Units/Rail
NE592D8R2G			2500 / Tape & Reel
NE592D14G		SOIC-14 (Pb-Free)	55 Units/Rail
NE592D14R2G			2500 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.



-X-

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⊕ 0. (0.010) ○ ○

-Y-

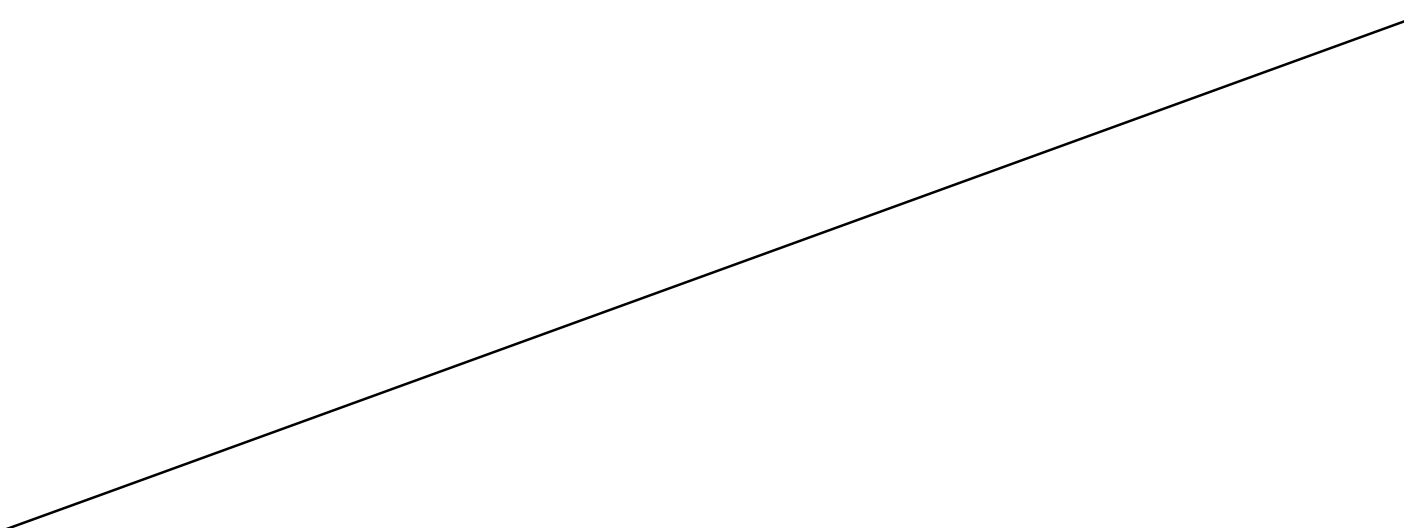
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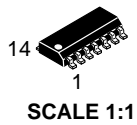
-Z-

C	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27 BSC		0.050 BSC	
H	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
M	0	8	0	8
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

0. (0.010) ○ 101100 1.000 0.1 1011. 100 0001.1 1001 1 0( )01.1 100111.1 10000 5.80 6.20 0.228 0.244 1.0 0 1000 0. )

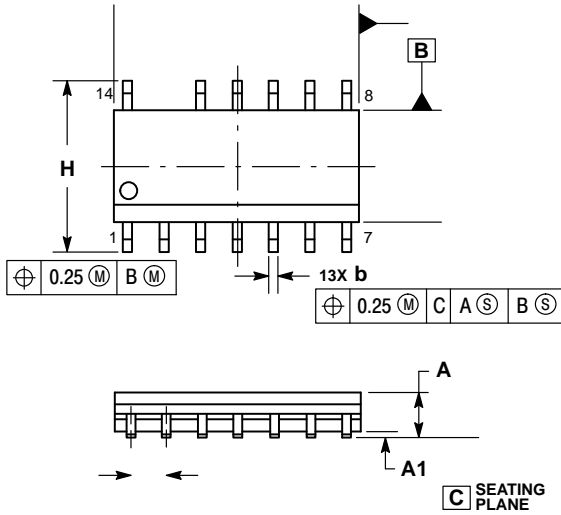






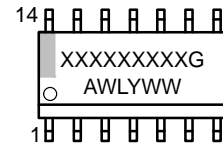
**SOIC 14 NB**  
CASE 751A-03  
ISSUE L

DATE 03 FEB 2016



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF AT MAXIMUM MATERIAL CONDITION.
  4. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSIONS.
  5. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.

**GENERIC MARKING DIAGRAM\***



- XXXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- Y = Year
- WW = Work Week
- G = Pb-Free Package

STYLES ON PAGE 2

**SOIC 14**  
CASE 751A-03  
ISSUE L

DATE 03 FEB 2016

STYLE 7:  
PIN 1. ANODE/CATHODE  
2. COMMON ANODE  
3. COMMON CATHODE  
4. ANODE/CATHODE  
5. ANODE/CATHODE

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