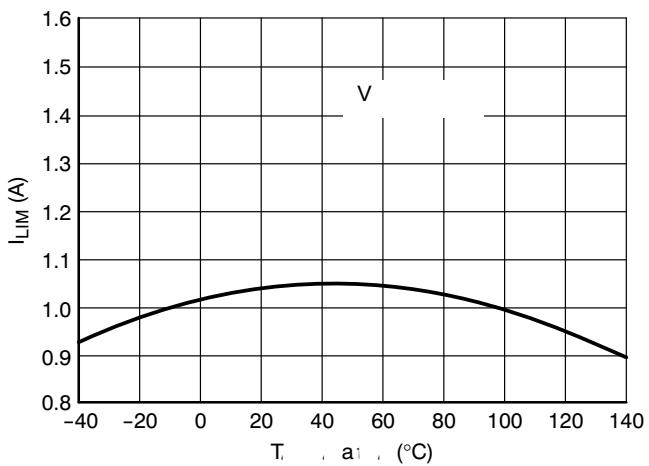
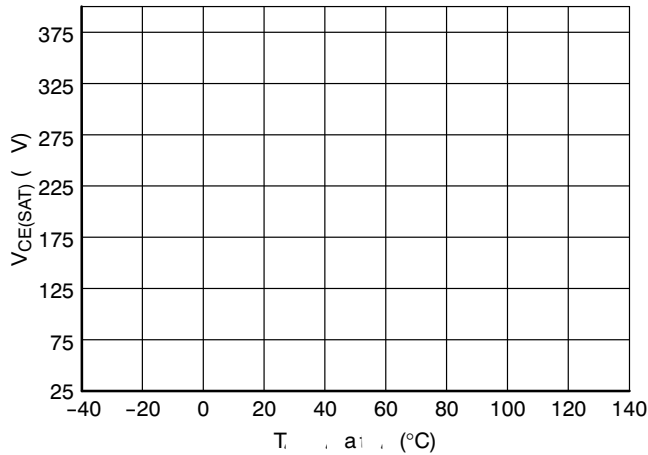
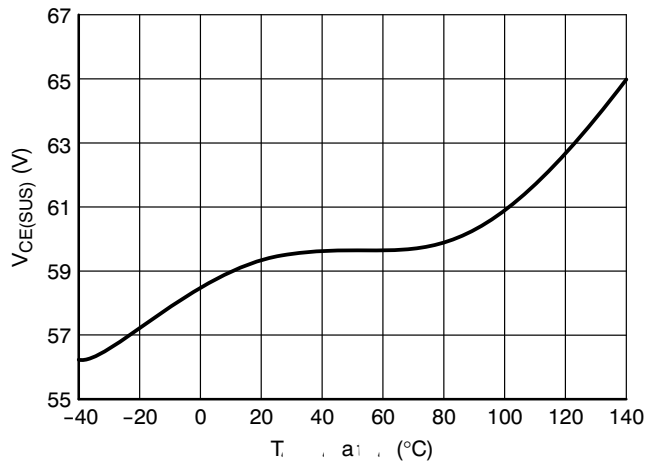


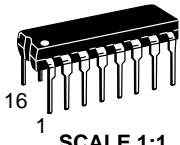
1.00 1.00 1.00

Pin Header

Pin #	Signal	Notes
1	OUTA	Differential Output A
2	CLAMPAB	Differential Clamp A
3	OUTB	Differential Output B
4	GND	Ground
5	GND	Ground
6	OUTC	Differential Output C
7	CLAMPAB	Differential Clamp B
8	OUTD	Differential Output D
9	IND	Indicator
10	INC	Increment
11	V _{CC}	5.0V Input Supply
12	GND	Ground
13	GND	Ground
14		



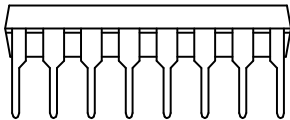
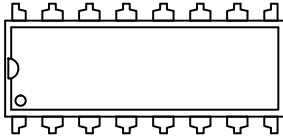




SCALE 1:1

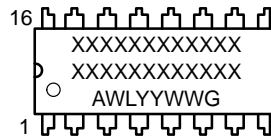
PDIP-16
CASE 648-08
ISSUE V

DATE 22 APR 2015



STYLE 1:

GENERIC MARKING DIAGRAM*



- XXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

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