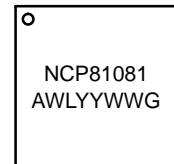
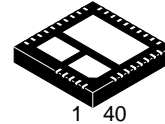


NCP81081

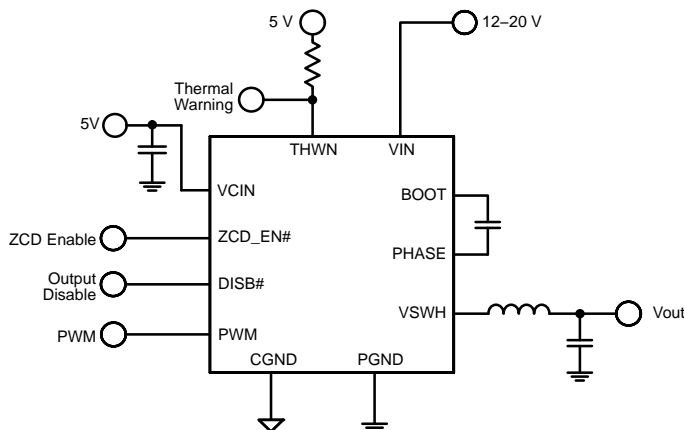
In regulated Diode and MOSFET



ON Semiconductor

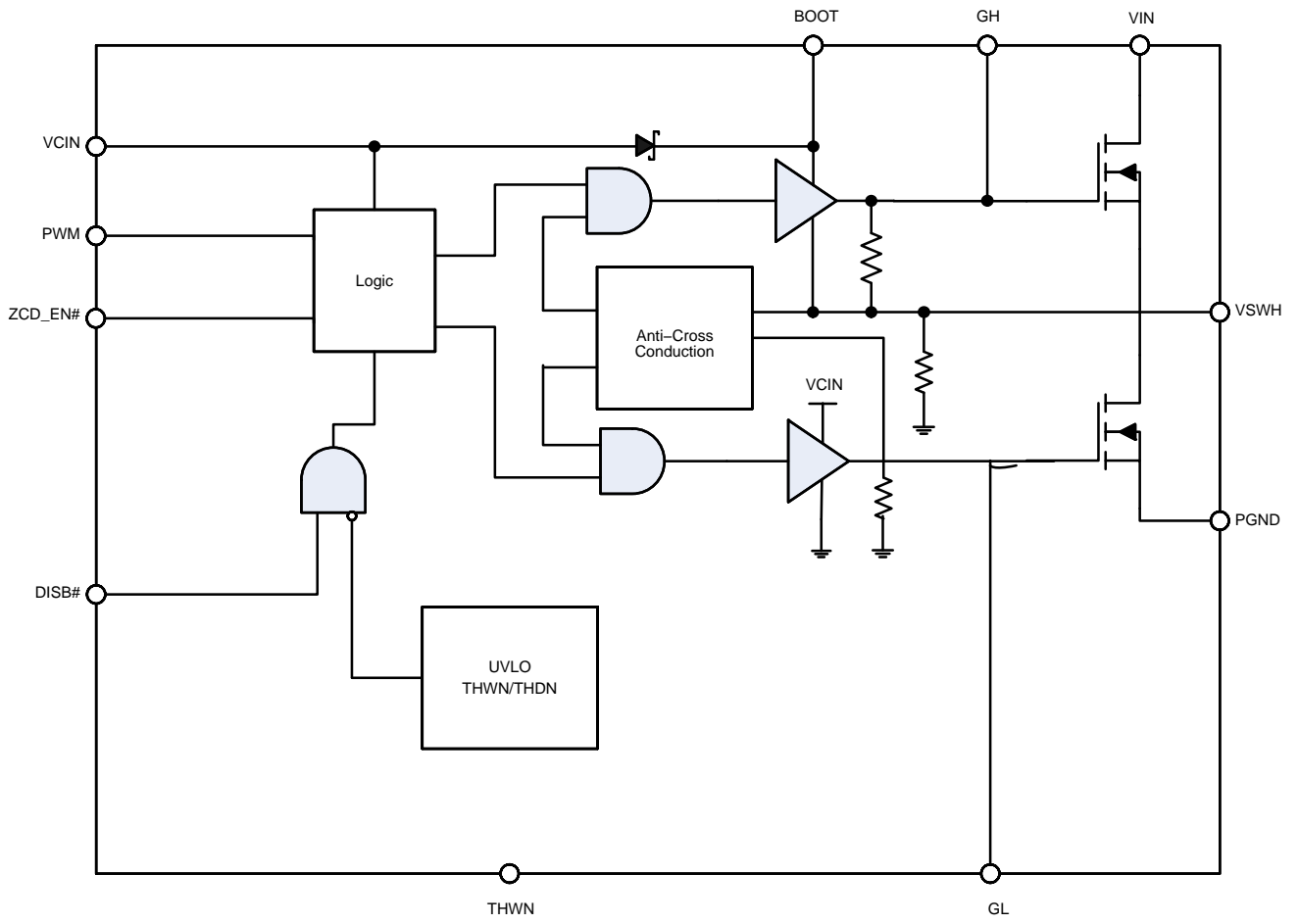


- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package



		†
NCP81081MNR2G	QFN40 (Pb-Free)	2500/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.



(Note 1) (VCIN = 5 V, VIN = 12 V, TA = -10°C to +100°C, unless otherwise noted)

--	--	--	--	--	--	--

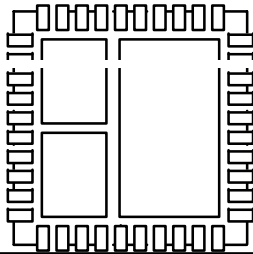
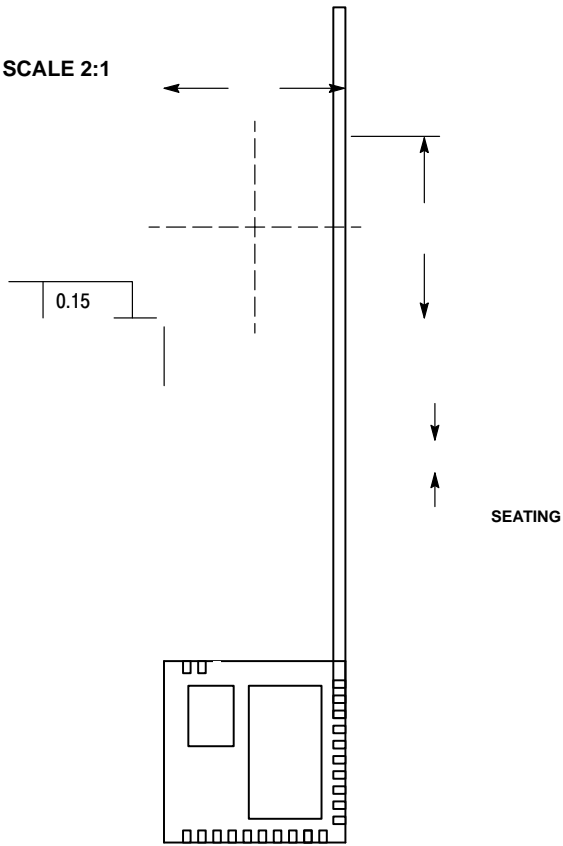
VCIN Current (normal mode)	-	DISB# = 5 V, PWM = OSC, FSW = 400 kHz		14	20
----------------------------	---	--	--	----	----

- -
- -
- -
- -

QFN40 6x6, 0.5P
CASE 485AZ
ISSUE 0

DATE 09 JAN 2009

SCALE 2:1



onsemi, **onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi**
