



MC14060B

Table 1. Truth Table

Clock	Reset	Output State
	L	No Change
	L	Advance to Next State
H	H	All Outputs are Low

X = Don't Care

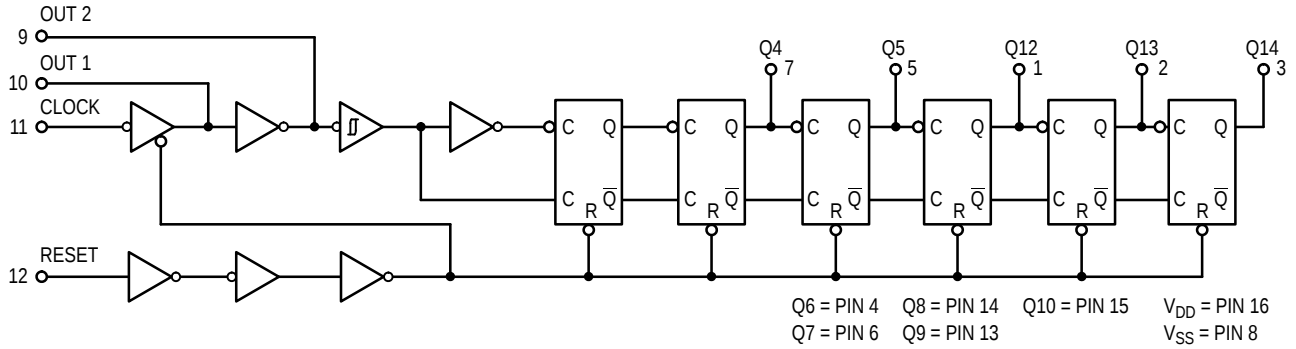


Figure 1. Logic Diagram

ORDERING INFORMATION

Device	Package	Shipping†
MC14060BDG	SOIC-16 (Pb-16f4 5.e.193 1.1055 TD:004T59.754 36814 .34015 ref9S)	

C Q

R

MC14060B

ELECTRICAL CHARACTERISTICS (Voltages Referenced to V_{SS})

Symbol	Characteristic	V_{DD} Vdc	55°C		25°C			125°C		Unit	
			Min	Max	Min	Typ (Note 2)	Max	Min	Max		
V_{OL}	Output Voltage $V_{in} = V_{DD}$ or 0	"0" Level	5.0	-	0.05	-	0	0.05	-	0.05	V
		10	-	0.05	-	0	0.05	-	0.05		
		15	-	0.05	-	0	0.05	-	0.05		
V_{OH}	$V_{in} = 0$ or V_{DD}	"1" Level	5.0	4.95	-	4.95					
		10	9.95	-	9.95						
		15	14.95	-	14.95						

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SWITCHING CHARACTERISTICS (C_L = 50 pF, T_A = 25°C)

Symbol	Characteristic	V _{DD} Vdc	Min	Typ (Note 5)	Max	Unit
t _{TLH}	Output Rise Time (Counter Outputs)	5.0	–	40	200	ns
		10	–	25	100	
		15	–	20	80	
t _{THL}	Output Fall Time (Counter Outputs)	5.0	–	50	200	ns
		10	–	30	100	
		15	–	20	80	
t _{PLH} t _{PHL}	Propagation Delay Time Clock to Q4	5.0	–	415	740	ns
		10	–	175	300	
		15	–	125	200	
	Clock to Q14	5.0	–	1.5	2.7	μs
		10	–	0.7	1.3	
		15	–	0.4	1.0	
t _{WH}	Clock Pulse Width	5.0	100	Uni47 6599 .9781 refBT8 0 0 8 396.5669 583.08		
		10	40			
		15	30			

MC14060B

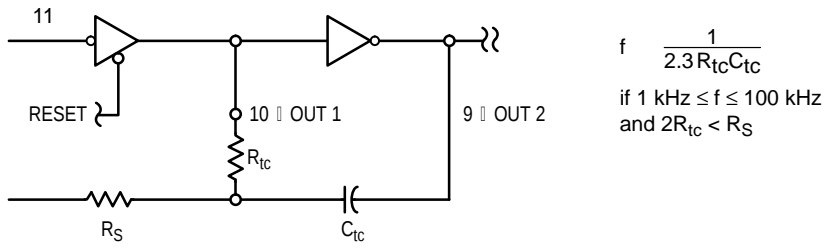


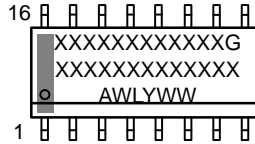
Figure 3. Oscillator Circuit Using RC Configuration

SOIC-16 9.90x3.90x1.50 1.27P
CASE 751B
ISSUE L

SOIC-16 9.90x3.90x1.50 1.27P
CASE 751B
ISSUE L

DATE 29 MAY 2024

GENERIC
MARKING DIAGRAM*



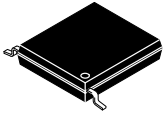
XXXXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
Y = Year
WW = Work Week
G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

<p>S 1: 1. C C ✓ 2. BAS ✓ 3. ✓ 4. C C ✓ 5. ✓ 6. BAS ✓ 7. C C ✓ 8. C C ✓ 9. BAS ✓ 10. ✓ 11. C C ✓ 12. ✓ 13. BAS ✓ 14. C C ✓ 15. ✓ 16. C C ✓</p>	<p>S 2: 1. CA ✓ 2. A ✓ 3. C C ✓ 4. CA ✓ 5. CA ✓ 6. C C ✓ 7. A ✓ 8. CA ✓ 9. CA ✓ 10. A ✓ 11. C C ✓ 12. CA ✓ 13. CA ✓ 14. C C ✓ 15. A ✓ 16. CA ✓</p>	<p>S 3: 1. C C , #1 ✓ 2. BAS , #1 ✓ 3. , #1 ✓ 4. C C , #1 ✓ 5. C C , #2 ✓ 6. BAS , #2 ✓ 7. , #2 ✓ 8. C C , #2 ✓ 9. C C , #3 ✓ 10. BAS , #3 ✓ 11. , #3 ✓ 12. C C , #3 ✓ 13. C C , #4 ✓ 14. BAS , #4 ✓ 15. , #4 ✓ 16. C C , #4 ✓</p>	<p>S 4: 1. C C , #1 ✓ 2. C C , #1 ✓ 3. C C , #2 ✓ 4. C C , #2 ✓ 5. C C , #3 ✓ 6. C C , #3 ✓ 7. C C , #4 ✓ 8. C C , #4 ✓ 9. BAS , #4 ✓ 10. , #4 ✓ 11. BAS , #3 ✓ 12. , #3 ✓ 13. BAS , #2 ✓ 14. , #2 ✓ 15. BAS , #1 ✓ 16. , #1 ✓</p>
<p>S 5: 1. A , #1 ✓ 2. A , #1 ✓ 3. A , #2 ✓ 4. A , #2 ✓ 5. A , #3 ✓ 6. A , #3 ✓ 7. A , #4 ✓ 8. A , #4 ✓ 9. A , #4 ✓ 10. S C , #4 ✓ 11. A , #3 ✓ 12. S C , #3 ✓ 13. A , #2 ✓ 14. S C , #2 ✓ 15. A , #1 ✓ 16. S C , #1 ✓</p>	<p>S 6: 1. CA ✓ 2. CA ✓ 3. CA ✓ 4. CA ✓ 5. CA ✓ 6. CA ✓ 7. CA ✓ 8. CA ✓ 9. A ✓ 10. A ✓ 11. A ✓ 12. A ✓ 13. A ✓ 14. A ✓ 15. A ✓ 16. A ✓</p>	<p>S 7: 1. S C -C ✓ 2. C A () ✓ 3. C A () ✓ 4. A -C ✓ 5. C A () ✓ 6. C A () ✓ 7. C A () ✓ 8. S C -C ✓ 9. S C -C ✓ 10. C A () ✓ 11. C A () ✓ 12. C A () ✓ 13. A -C ✓ 14. C A () ✓ 15. C A () ✓ 16. S C -C ✓</p>	

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SCALE 2:1

TSSOP-16 WB
CASE 948F
ISSUE B

DATE 19 OCT 2006

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