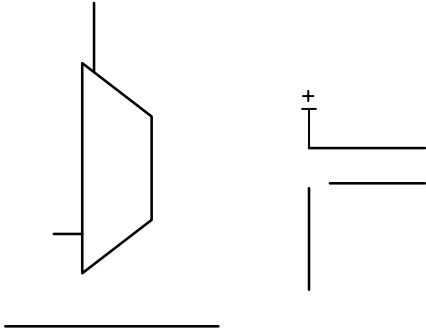


MC100EP809



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Table 8. AC CHARACTERISTICS $V_{CCI} = 3.0\text{ V to }3.6\text{ V}$; $V_{CCO} = 1.6\text{ V to }2.0\text{ V}$, $GND = 0\text{ V}$ (Note 5)

Symbol	Characteristic	0 C			25 C			85 C			Unit
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V_{Opp}	Differential Output Voltage (Figure 3)										
	$f_{out} < 100\text{ MHz}$	600	850		600	850		600	850		mV
	$f_{out} < 500\text{ MHz}$	600	750		600	750		600	750		mV
	$f_{out} < 750\text{ MHz}$	450	575		450	575		450	575		mV
t_{PLH} t_{PHL}	Propagation Delay (Differential Configuration)										
	LVPECL_CLK to Q	680	800	930	700	820	950	780	920	1070	ps
	HSTL_CLK to Q	690	830	990	700	850	1000	790	950	1110	ps

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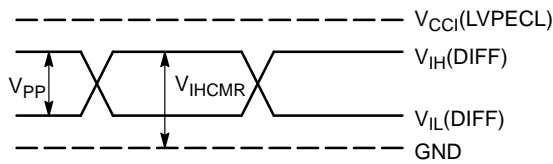


Figure 4. LVPECL Differential Input Levels

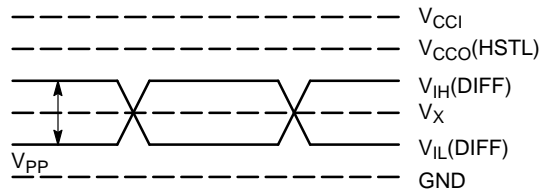


Figure 5. HSTL Differential Input Levels

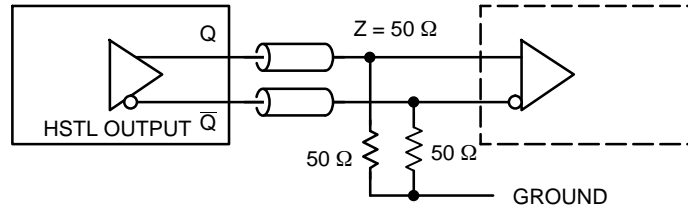
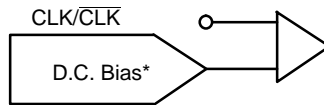
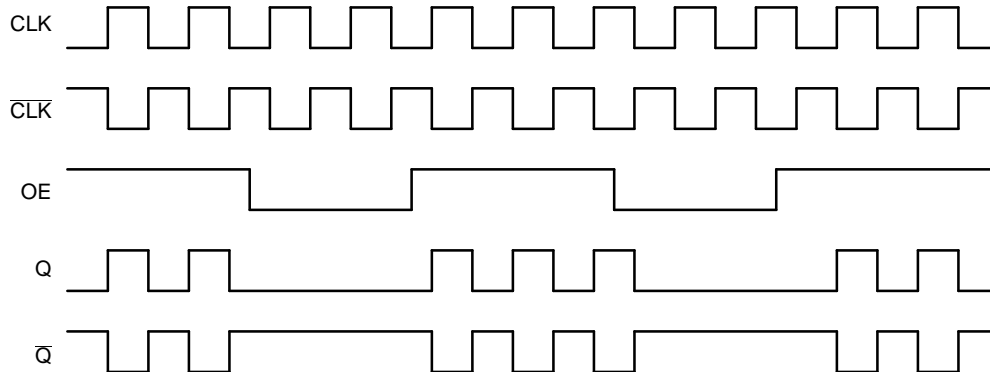


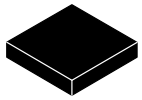
Figure 6. HSTL Output Termination and AC Test Reference



*Must be CLK/CLK common mode voltage: $((V_{IH} + V_{IL})/2)$.

Figure 7. Single-Ended CLK/CLK Input Configuration

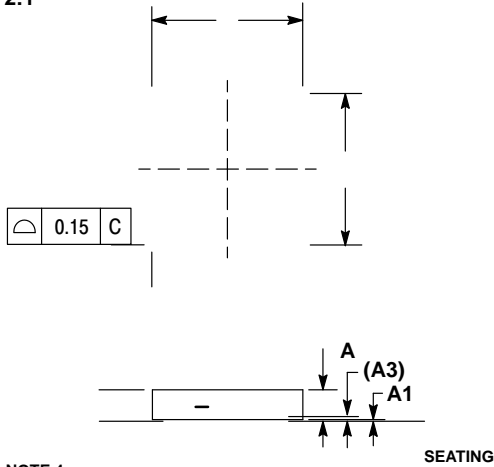




QFN32 5x5, 0.5P
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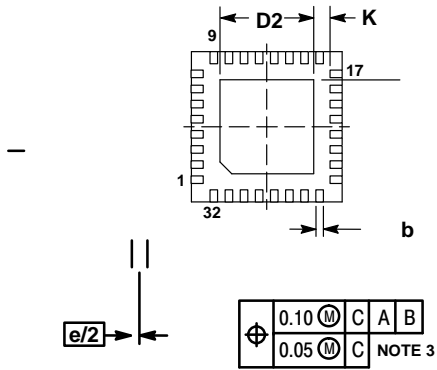
DATE 23 OCT 2013

SCALE 2:1



NOTE 4

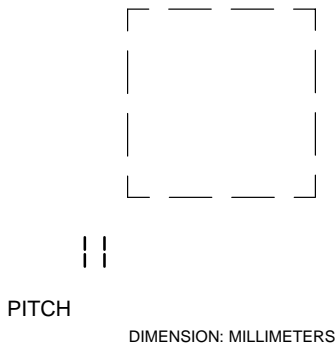
	MAX
A1	0.80 1.00
A3	0.20 REF 0.05
b	0.18 0.30
D	5.00 BSC
D2	2.95 3.25
E	5.00 BSC
E2	2.95 3.25
e	0.50 BSC
K	0.20
L	0.30 0.50
L1	0.15



XXXXXXXXXX
XXXXXXXXXX
AWLYYYWW■

■Free indicator, "G" or

RECOMMENDED



PITCH

DIMENSION: MILLIMETERS

DOCUMENT NUMBER:	98AON20032D	

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