

Summary of Specification for OIS & CL-AF Control LSI

LC898129DP1XHTBG

Overview

LC898129DP1XHTBG is a system LSI integrating an on-chip 32 bit DSP, a FLASH ROM and peripherals including analog circuits for OIS (Optical Image Stabilization) / Closed Loop-AF (Auto Focus) control and drivers.

Features

- On-chip 32 bit DSP
 - ◆ Built-in Software for Digital Servo Filter
 - ◆ Built-in Software for Gyro Filter
- Memory
 - ◆ Flash Memory
 - ◆ Program ROM
 - ◆ Program SRAM
 - ◆ Data SRAM
- Peripherals
 - ◆ AD Converter
 - ◆ DA Converter
 - ◆ 2-wire Serial I/F Circuit (The Communication Protocol is Compatible with I²

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Table 1. PIN DESCRIPTION

No.	Pin	I/O	I/O Pwr	Function	Init
					-
					-
					-
					-
					-
				-	
				-	

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ELECTRICAL CHARACTERISTICS

Table 2. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Conditions	Ratings	Unit
		$\leq \circ$	-	
		$\leq \circ$	-	
		$\leq \circ$	-	
		$\leq \circ$	-	
		$\leq \circ$	-	
		$\leq \circ$	-	
			-	\circ

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Table 4. D.C. CHARACTERISTICS: INPUT/OUTPUT

Parameter	Symbol	Conditions	Min	Typ	Max	Unit	Applicable Pins
-				-	-		
-			-	-			
-				-	-		
-							

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AC CHARACTERISTICS

2 wire Serial Interface Timing

The 2-wire serial interface timing definition and electric characteristics are shown below. The communication protocol is compatible with I²C. This circuit has clock stretch function.

Static Address : 7'b0100100

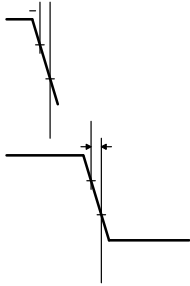
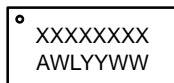


Figure 5.

WLCSP40 1.60x4.15x0.33
CASE 567XS
ISSUE O

DATE 12 APR 2019

**GENERIC
MARKING DIAGRAM***



XXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
YY = Year
WW = Work Week

*This information is generic. Please refer to

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