

6-Pin DIP Schmitt Trigger Quad Package

H11L1M, H11L2M, H11L3M

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Installation Classifications per DIN VDE 0110/1.89 Table 1, For For Rated Mains Voltage		< 150 V _{RMS}	I-IV
		< 300 V _{RMS}	I-IV
Climatic Classification		55/100/21	
Pollution Degree (DIN VDE 0110/1.89)		2	
Comparative Tracking Index		175	

V _{PR}	Input-to-Output Test Voltage, Method A, V _{IORM} × 1.6 = V _{PR} , Type and Sample Test with t _m = 10 s, Partial Discharge < 5 pC	1360	V _{peak}
	Input-to-Output Test Voltage, Method B, V _{IORM} × 1.875 = V _{PR} , 100% Production Test with t _m = 1 s, Partial Discharge < 5 pC	1594	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	850	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥7	mm
	External Clearance	≥7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥0.5	mm
T _S	Case Temperature (Note 1)	175	°C
I _{S,INPUT}	Input Current (Note 1)	350	mA
P _{S,OUTPUT}	Output Power (Note 1)	800	mW
R _{IO}	Insulation Resistance at T _S , V _{IO} = 500 V (Note 1)	>10 ⁹	Ω

1. Safety limit values – maximum values allowed in the event of a failure.

--	--	--	--

T _{STG}	Storage Temperature	-40 to +125	°C
T _{OPR}	Operating Temperature	-40 to +85	°C
T _J	Junction Temperature	-40 to +125	°C
T _{SOL}	Lead Solder Temperature	260 for 10 seconds	°C
P _D	Total Device Power Dissipation at 25°C Derate Above 25°C	250	mW
		2.94	mW/°C

I _F	Continuous Forward Current	30	mA
V _R	Reverse Voltage	6	V
I _{F(pk)}	Forward Current – Peak (1 μs pulse, 300 pps)	100	mA
P _D	LED Power Dissipation	60	mW

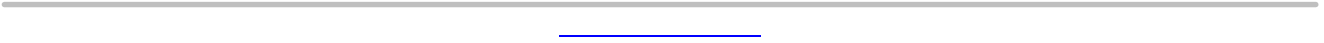
P _D	Detector Power Dissipation	150	mW
V _O	V ₄₅ Allowed Range	0 to 16	V
V _{CC}	V ₆₅ Allowed Range	3 to 16	V
I _O	I ₄ Output Current	50	mA

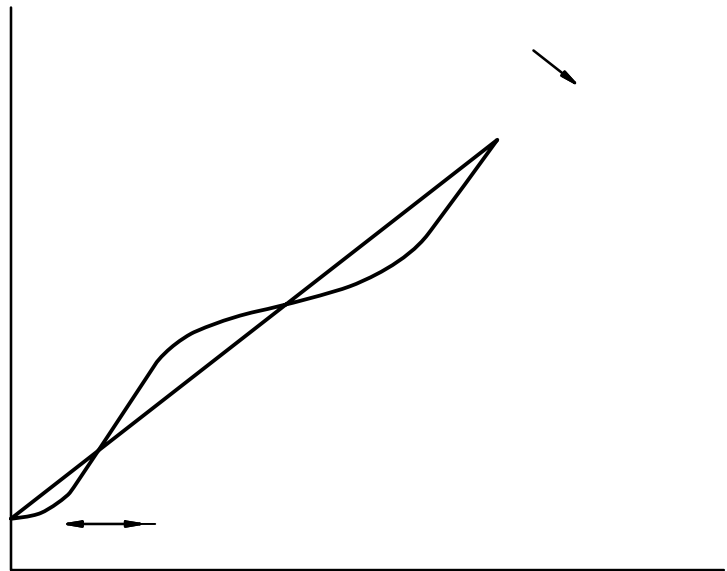
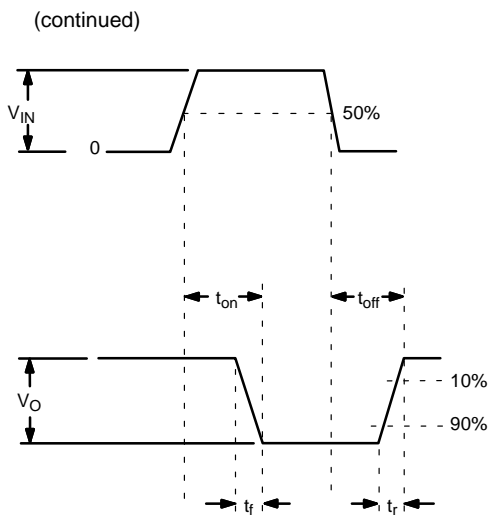
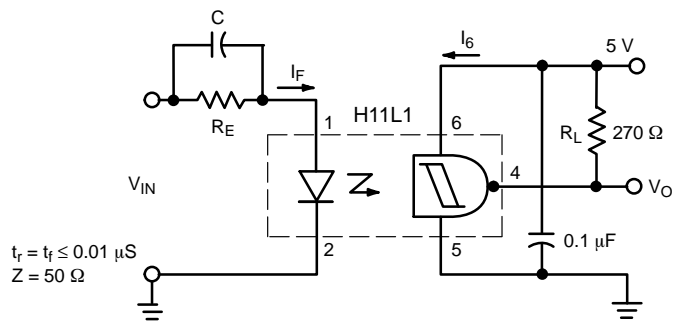
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

(T_J = 25°C unless otherwise noted)

--	--	--	--	--	--	--	--

V _F	Input Forward Voltage	I _F = 10 mA	All		1.2	1.5	V
		I _F = 0.3 mA		0.75	1.0		
I _R	Reverse Current	V _R = 3 V	All			10	μA
C _J							





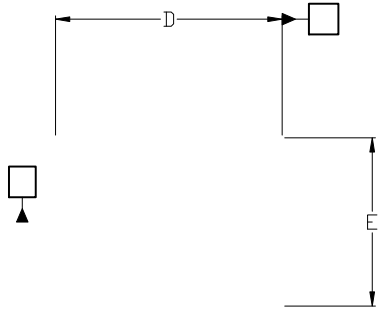
PDIP6 8.51x6.35, 2.54P
CASE 646BX
ISSUE O

DATE 31 JUL 2016

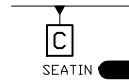
NOTES:

A) NO STANDARD APPL

PDIP6 8.51x6.35, 2.54P

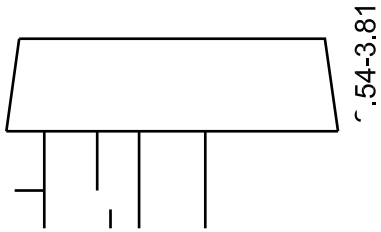
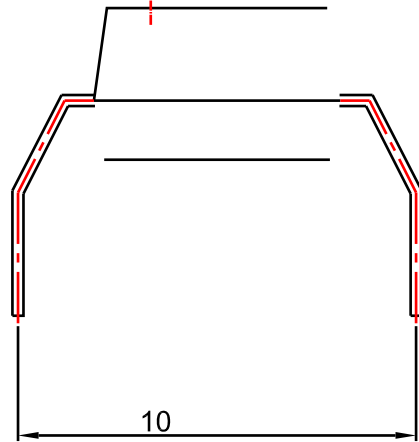
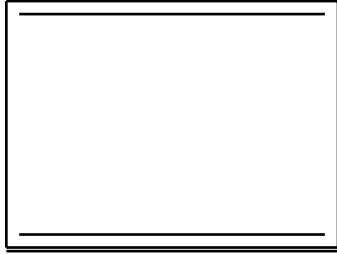


TOP VIEW



PDIP6 8.51x6.35, 2.54P
CASE 646BZ
ISSUE O

DATE 31 JUL 2016



NOTES:
A) NC

.33

-

•

onsemi, **onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi**
