

Is Now Part of



To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. *Typicalisma (31()):. *Typicalisma (



EMI O

A st 9

FMS6 Low-

Featur

- Š Thr Re
- Š Sy
- Š T
- Š
- Š
- Š
- Š

		_ ~~~		
	1	and the same of th		A Contraction
OE —			00	or out can
OL				

			Eco Status	Package	Packing Method
FMS6203MTC1400X	-40°C to +85°C	0dB	RoHS	TSSOP-14	Tape and Reel
FMS6203MTC1406X	-40°C to +85°C	6dB	RoHS	TSSOP-14	Tape and Reel

For Fairchild's definition of Eco Status, please visit:

Pin Configuration	
	LOW
	-6030, 0-
	Cost, o-Cilaillei,
	AIGGO
	0

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V _{CC}	DC Supply Voltage	-0.3	6.0	V
V _{IO}	Analog and Digital I/O	-0.3	V _{CC} +0.3	V
I _{OUT}	Output Current, Any One Channel, Do Not Exceed		50	mA

Reliability Information

Symbol	Parameter	Min.	Тур.	Max.	Unit
TJ	Junction Temperature			+150	°C
T _{STG}	Storage Temperature Range	-65		+150	°C
TL	Lead Temperature, Soldering 10 Seconds			+300	°C
ΘЈΑ	Thermal Resistance, JEDEC Standard Multi-Layer Test Boards, Still Air		97		°C/W

Electrostatic Discharge Information

Symbol	Parameter	Max.	Unit
ESD	Human Body Model, JESD22-A114	8	kV
ESD	Charged Device Model, JESD22-C101	2	K V

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Тур.	Max.	Unit
T _A	Operating Temperature Range	-40		+85	°C

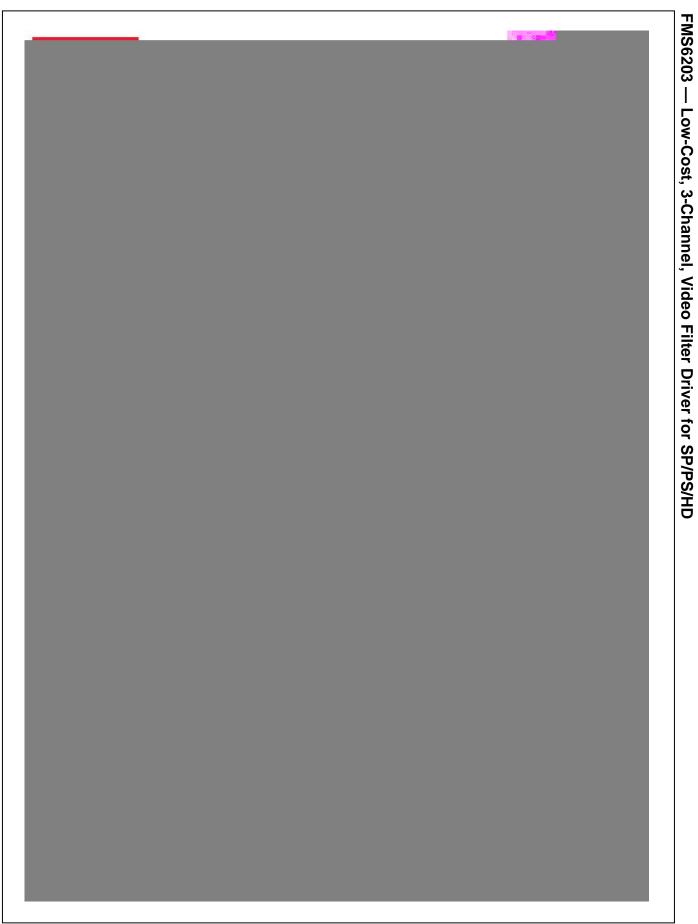
High-Definition Electrical Characteristic

Unless otherwise noted, $T_A=25^{\circ}C$, $V_{IN}=1V_{PP}$, $V_{CC}=5V$, $R_{SOURCE}=37.5\Omega$, inputs AC coupled with $0.1\mu F$, all outputs AC coupled with $220\mu F$ into 150Ω loads, referenced to 400kHz, all gain options.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
AV _{HD}	Channel Gain Error ⁽⁷⁾		-0.4	0	0.4	dB
f _{1dBHD}	-1dB Bandwidth ⁽⁷⁾		28	31		MHz

f

eneral layout and	supply bypassing pla performance	y a major role		
mign-nequency	репоппансе а	มาน เทษเทโสเ		



ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at https://www.nemiconductor.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor and other convey any license under its patent rights nor the rights of others. ON Semiconductor are other convey any license under its patent rights nor the rights of others. ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and application, Buyer shall indemnify and hold ON Semiconductor and its officers, emp