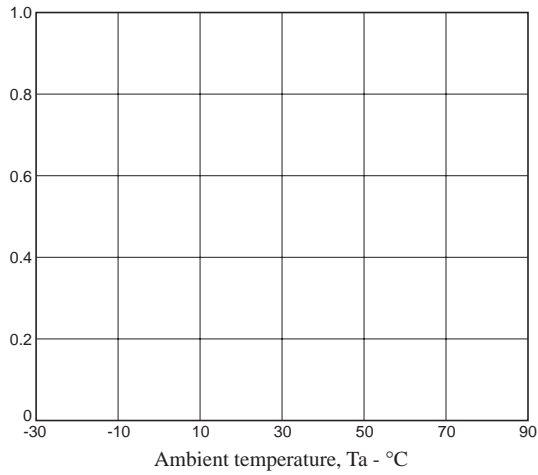




<http://onsemi.com>





### Pin Assignment

### Truth Table and Control Functions

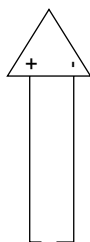
	Source→Sink	Hall input			FR
		U	V	W	
1	V → W	H	H	L	H
	W → V				L
2	U → W	H	L	L	H
	W → U				L
3	U → V	H	L	H	H
	V → U				L
4	W → V	L	L	H	H
	V → W				L
5	W → U	L	H	H	H
	U → W				L
6	V → U	L	H	L	H
	U → V				L

Note : The "H" state for FR is defined as a voltage of 8V or higher, and the "L" state for FR is defined as a voltage of 4V or lower. (When  $V_{CC} = 12V$ .)

Note : For the Hall inputs, the input high state is defined to be the state where the (+) input is higher than the corresponding (-) input by 0.01V or higher, and the input low state is defined to be the state where the (+) input is lower than the corresponding (-) input by 0.01V or higher.

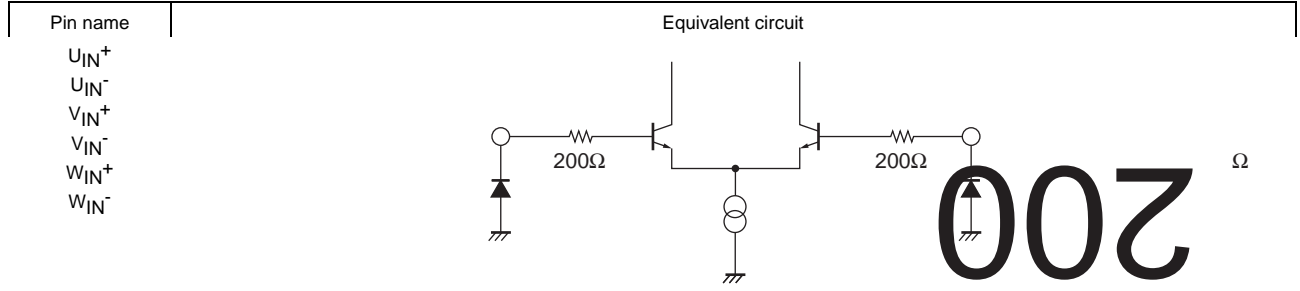
Note : Since this drive technique is a 180° current application scheme, the phases other than the sink and the source phases will not turn off.

Block Diagram



# LB11988H

## Equivalent Circuit



# LB11988H

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## Pin Functions

Pin No.	Pin name	Function
22 FRAME	GND	Ground for circuits other than the output transistors. Note that the Rf pin will be at the lowest potential of the output transistors.