

**2 4-C**  
**L C**  
**E D P A**

**CM1224**

The CM1224 family of diode arrays has been designed to provide ESD protection for electronic components or subsystems requiring minimal capacitive loading. These devices are ideal for protecting systems with high data and clock rates or for circuits requiring low capacitive loading. Each ESD channel consists of a pair of diodes in series which steer the positive or negative ESD current pulse to either the positive ( $V_P$ ) or negative ( $V_N$ ) supply rail. A Zener diode is embedded between  $V_P$  and  $V_N$ , offering two advantages. First, it protects the  $V_{CC}$  rail against ESD strikes, and second, it eliminates the need for a bypass capacitor that would otherwise be needed for absorbing positive ESD strikes to ground. The CM1224 will protect against ESD pulses up to 8 kV per the IEC 61000 4 2 standard.

These devices are particularly well suited for protecting systems using high speed ports such as USB 2.0, IEEE1394 (Firewire , iLink™), Serial ATA, DVI, HDMI and corresponding ports in removable storage, digital camcorders, DVD RW drives and other applications where extremely low loading capacitance with ESD protection are required.

The CM1224 family of devices has lead free finishing in a small package footprint.

Two or Four Channels of ESD Protection

Provides ESD Protection to IEC61000 4 2 Level 4

8 kV Contact Discharge

Low Channel Input Capacitance of 0.7 pF Typical

Minimal Capacitance Change with Temperature and Voltage

NOT RECOMMENDED FOR NEW DESIGN  
 PLEASE CONTACT YOUR REPRESENTATIVE FOR INFORMATION





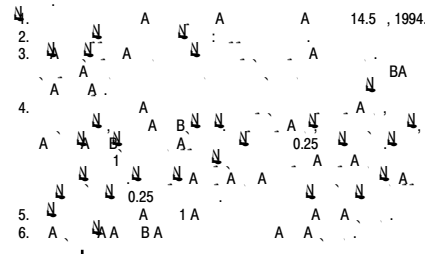
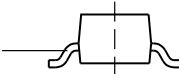
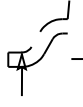
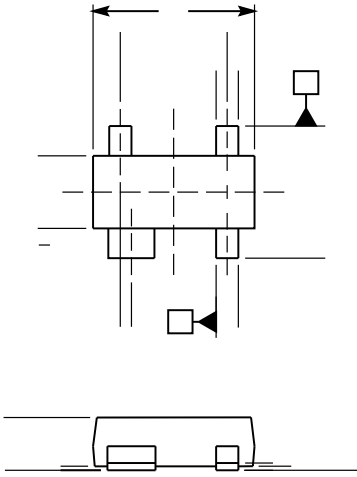




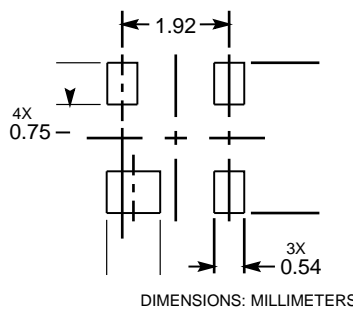
**DIS**



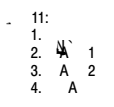
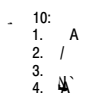
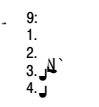
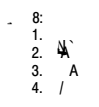
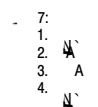
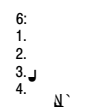
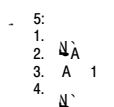
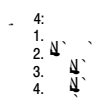
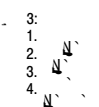
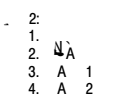
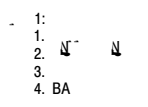
CASE 318A



MILLIMETERS		
DIM	MIN	MAX
A	0.80	1.12
A1	0.01	0.15
b	0.30	0.51
b1	0.76	0.94
c	0.08	0.20
D	2.80	3.05
E1	1.20	1.40
e	1.92 B	
e1	0.20 B	
L	0.35	0.70
L2	0.25 B	



(Note: Microdot may be in either location)



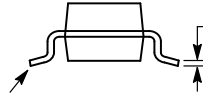
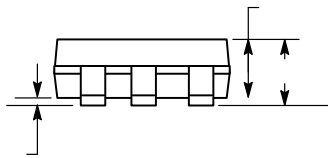
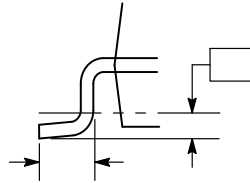
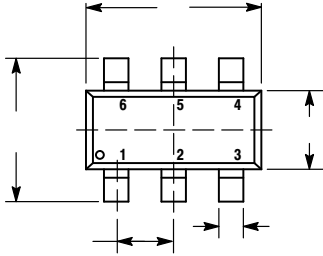




SCALE 2:1

CASE 527AJ  
ISSUE B

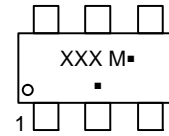
DATE 29 FEB 2012



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DATUM C IS THE SEATING PLANE.

DIM	MILLIMETERS	
	MIN	MAX
A	---	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.20	0.50
c	0.08	0.26
D	2.70	3.00
E	2.50	3.10
E1	1.30	1.80
e	0.95 BSC	
L	0.20	0.60
L2	0.25 BSC	



- XXX = Specific Device Code
- M = Date Code
- = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

MSOP10, 3x3



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**GENERIC  
MARKING DIAGRAM\***

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