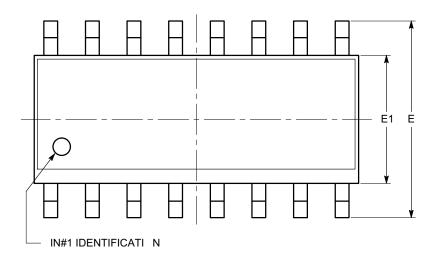


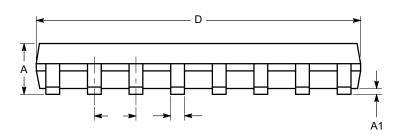


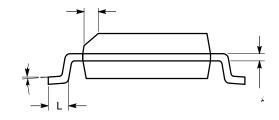
	<del> </del>	
$V_{CC}$		

**DATE 19 DEC 2008** 



## **TOP VIEW**





**END VIEW SIDE VIEW** 

## Notes:

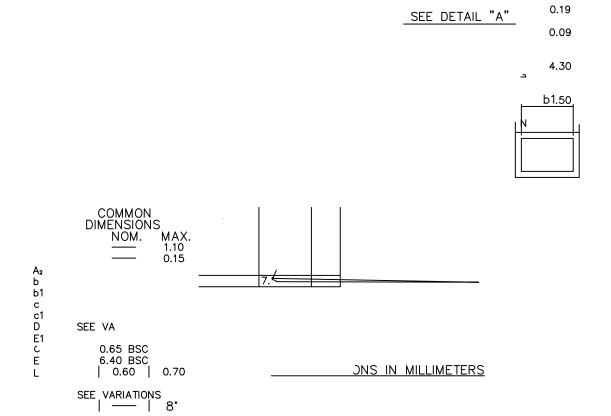
- (1) A ... n n .. na , ... n m... m , . An ... n , ... (2) C m ... . JEDEC MS-012.

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