

May 1993 Revised October 2003

74LVX157

Low Voltage Quad 2-Input Multiplexer

General Description

The LVX157 is a high-speed quad 2-input multiplexer. Four bits of data from two sources can be selected using the common Select and Enable inputs. The four outputs present the selected data in the true (noninverted) form. The LVX157 can also be used as a function generator.

Features

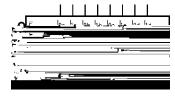
- Input voltage level translation from 5V to 3V
- Ideal for low power/low noise 3.3V applications
- Guaranteed simultaneous switching noise level and dynamic threshold performance

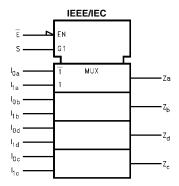
Ordering Code:

Order Number	Package Number	Package Description
74LVX157M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
74LVX157SJ	M16D	16-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74LVX157MTC	MTC16	16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

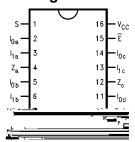
Devices are also available in Tape and Reel. Specify by appending letter suffix "X" to the ordering code.

Logic Symbols



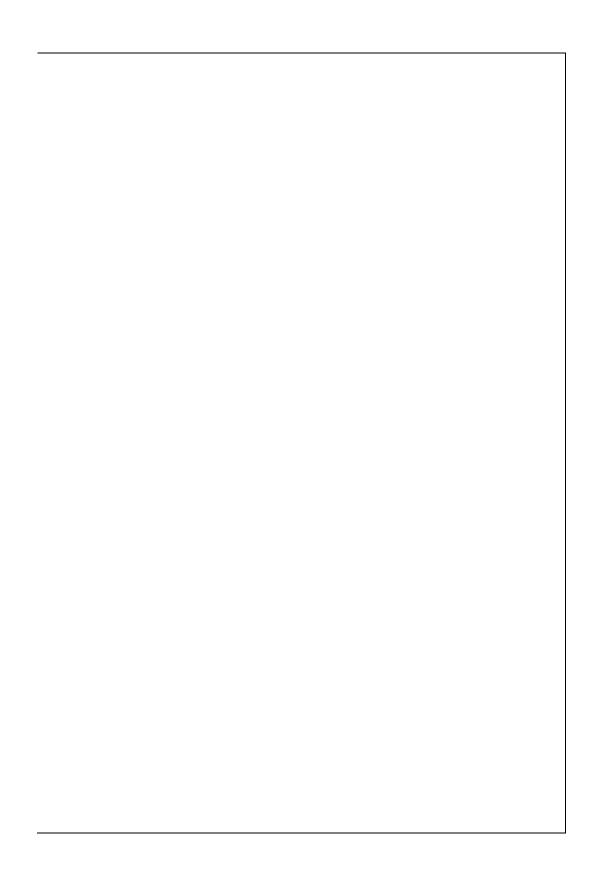


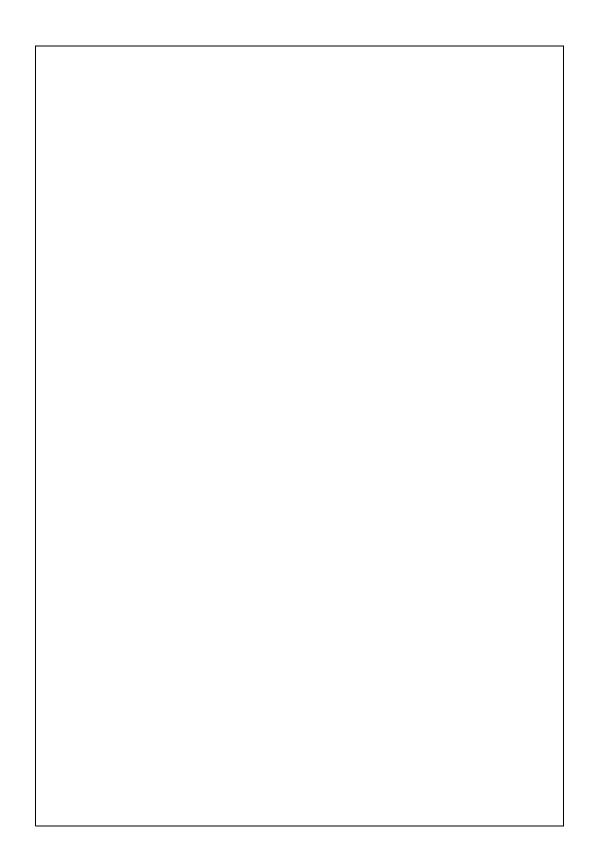
Connection Diagram

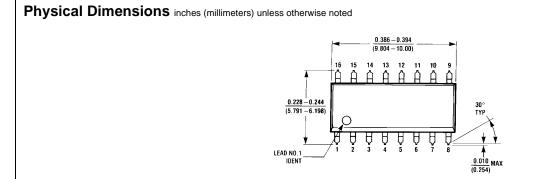


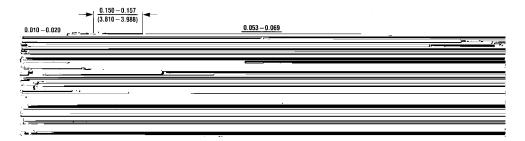
Pin Descriptions

Pin Names	Description
$I_{0a}-I_{0d}$	Source 0 Data Inputs
I _{0a} -I _{0d} I _{1a} -I _{1d} E	Source 1 Data Inputs
Ē	Enable Input
S	Select Input
Z _a –Z _d	Outputs

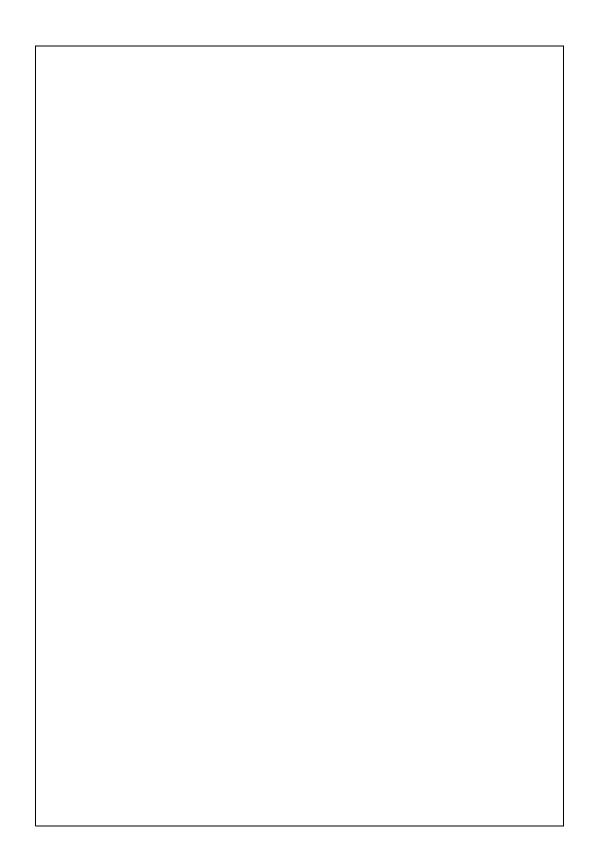


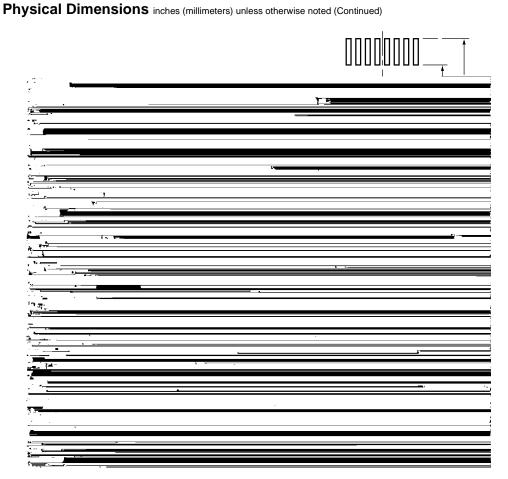






16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow Package Number M16A





16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide Package Number MTC16

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